Manual HR series

hr25*CL

Company Information

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This Operation Manual is based on the following standards:

DIN EN 62079
DIN EN ISO 12100
ISO Guide 37
DIN ISO 3864-2
DIN ISO 3864-4

This Operation Manual contains important instructions for safe and efficient handling of SVCam Cameras (hereinafter referred to as „camera“). This Operating Manual is part of the camera and must be kept accessible in the immediate vicinity of the camera for any person working on or with this camera.

Read carefully and make sure you understand this Operation Manual prior to starting any work with this camera. The basic prerequisite for safe work is compliant with all specified safety and handling instructions.

Accident prevention guidelines and general safety regulations shoud be applied.

Illustrations in this Operation Manual are provided for basic understanding and can vary from the actual model of this camera. No claims can be derived from the illustrations in this Operation Manual.

The camera in your possession has been produced with great care and has been thoroughly tested. Nonetheless, should you have reasons for complaint, then please contact your local SVS-VISTEK distributor. You will find a list of distributors in your area under: http://www.svs-vistek.com/company/distributors/distributors.php

Copyright Protection Statement

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1 Safety Messages

The classification of hazards is made pursuant to ISO 3864-2 and ANSI Y535.6 with the help of key words.

This Operating Manual uses the following Safety Messages:

Risk of death or serious injury	DANGER	Danger indicates a hazard with a high level of risk which, if not avoided will result in death or serious injury.
	WARNING	Warning indicates a hazard with a medium level of risk which, if not avoided will result in death or serious injury.
	CAUTION	Caution indicates a hazard with a low level of risk which, if not avoided will result in death or serious injury.
Risk of damage	PROHIBITION	A black graphical symbol inside a red circular band with a red diagonal bar defines a safety sign that indicates that an action shall not be taken or shall be stopped.
	CAUTION!	A black graphical symbol inside a yellow triangle defines a safety sign that indicates a hazard.
	MANDATORY ACTION	A white graphical symbol inside a blue circle defines a safety sign that indicates that an action shall be taken to avoid a hazard.
Cross-reference	NOTICE	Provides references and tips

Figure 1: Safety messages

2 Legal Information

Information given within the manual accurate as to: July 23, 2018, errors and omissions excepted.

These products are designed for industrial applications only. Cameras from SVS-Vistek are not designed for life support systems where malfunction of the products might result in any risk of personal harm or injury. Customers, integrators and end users of SVS-Vistek products might sell these products and agree to do so at their own risk, as SVS-Vistek will not take any liability for any damage from improper use or sale.

Europe

This camera is CE tested, rules of EN 55022:2010+AC2011 and EN61000-6-2:2005 apply.

The product is in compliance with the requirements of the following European directives:

2014/30/EU	Electromagnetic compatibility (EMC)
2011/65/EU	Restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS)

All SVS-VISTEK cameras comply with the recommendation of the European Union concerning RoHS Rules

USA and Canada

This device complies with part 15 of the FCC Rules. Operation is subject to the following conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Warning: This equipment is compliant with Class A of CISPR 32. In a residential environment this equipment may cause radio interference.

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules.

It is necessary to use a shielded power supply cable. You can then use the “shield contact” on the connector which has GND contact to the camera housing. This is essential for any use. If not done and camera is destroyed due to Radio Magnetic Interference (RMI) WARRANTY is void!

- Power: US/UK and European line adapter can be delivered. Otherwise use filtered and stabilized DC power supply.
- Shock & Vibration Resistance is tested: For detailed Specifications refer to Specification.

3 Getting Started

3.1 Contents of Camera Set

> Camera
> Power supply (if ordered/option)
> Quick guide
> User Manual
> Software installer – SVCam Kit
> Euresys camera file (optional)

3.2 Power supply

Connect the power supply with the Hirose connector.

CAUTION! – This camera does not support hotplugging

1. First, connect the data cable.
2. Then connect power supply.

When using your own power supply (voltage range 10 -25 V DC) see also Hirose 12-pin for a detailed pin layout of the power connector. For power input specifications refer to specifications.

3.3 Camera status LED codes

On power up, the camera will indicate its current status with a flashing LED on its back. The LED will change color and rhythm.

The meaning of the blinking LED status codes translates as follows:

Figure 1: Status LED flashing codes

Flashing	Description
Yellow quickly (≈ 8 Hz)	booting
Yellow permanent	ready
Red slow (≈ 1 Hz)	error

3.4 Software

Further information, documentations, release notes, latest software and application manuals can be downloaded in the download area on SVS-Vistek's download area. Depending on the type of camera you bought, several software packages apply.

3.4.1 SVCapture 2

SVCapture 2.x is a XML based software tool. It provides the possibility to control a GenICam based camera. The image result of any modification of a camera's adjustment is immediately visible, making it the ideal tool to optimize camera adjustments.

SVCapture is included in the SVCam Kit, you can download it for free from SVS-Vistek's download area.

Please refer to the SVCam Kit Install guide for details. You will find this document in the download area as well.

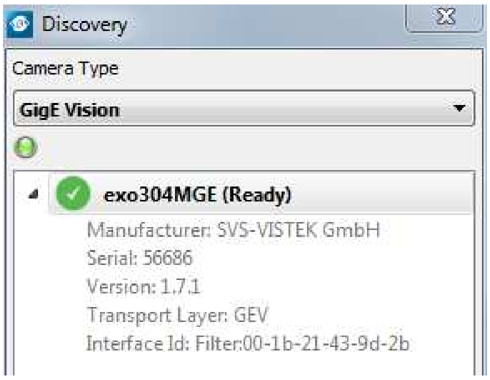
Generally, any GenICam based software package should be able to run a SVS-Vistek camera (GigE Vision, USB3, Camera Link).

It is strongly recommended to uninstall the existing version of SVCam Kit or SVCapture before installing the new version. While installing, please deactivate your firewall and antivirus programs.

Quick guide install

- Download the SVCam Kit matching to your operating system
- Disable firewall and antivirus programs
- Unpack and install the software and the drivers required for your camera's interface type
- Enable firewall and antivirus programs

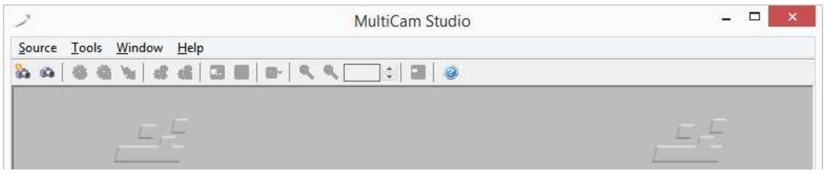
Connect your camera's interface cable and power. Start SVCapture. Select your interface type in the discovery dialogue, SVCapture should show your camera after about 30 seconds.



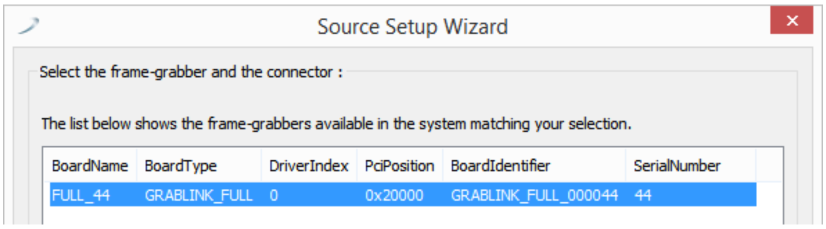
3.4.2 Camera Link Viewer Software

SVCam Kit is the best tool for modifying GenICam attributes. For viewing the resulting image you need external software. In most cases this will be the software delivered with your frame grabber. Assuming an Euresys framegrabber as an example, these are the steps for MultiCam Studio to display a camera image.

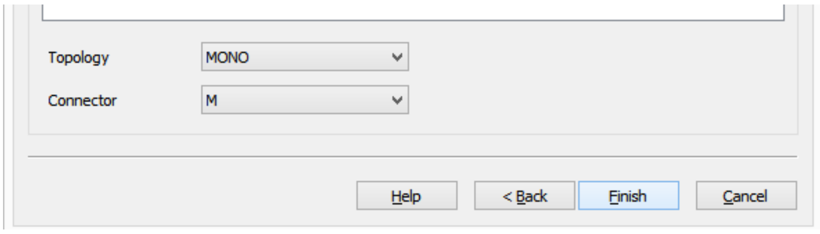
Run Multicam Studio.



> Add a new "source" to the application
> Choose "Camera Link industrial Camera…"
> Click "next"
> In the list of camera vendors choose "SVS-VISTEK" and the camera you want to view.
> Select frame grabber and connector



> For "Topology" values refer to the Euresys documentation. At first: stay with "Mono" for topology



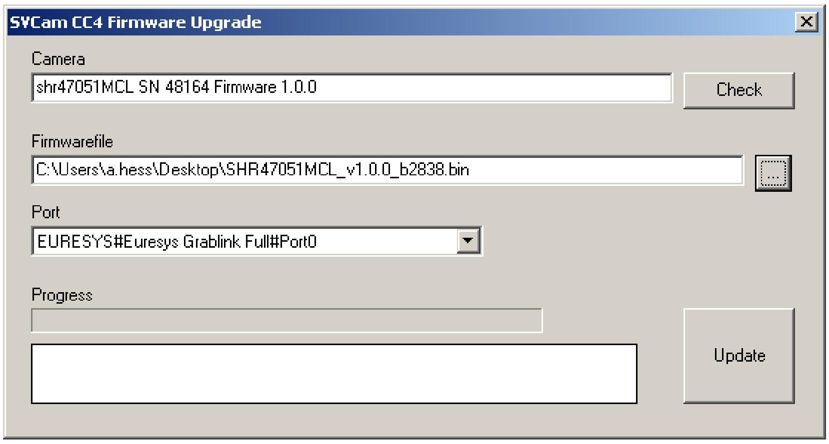
> Choose your connector configuration
> click "Finish"

According to your GenICam configuration, you should see the camera's image displayed.

For further information on Euresys Multicam Studio refer to the Euresys documentation.

3.4.3 Firmware updater

Some features may not have been implemented in your camera at the time of selling. For updating your camera firmware to the most recent version, you need to download the firmware upgrade tool "CL FirmwareUpdater" and the firmware file (download it from website, login area) matching your camera model.

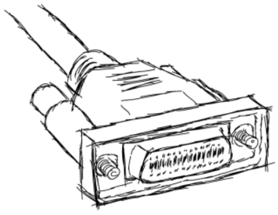


Execute firmware update

- Unpack upgrade tool and the correct firmware file into any folder, e.g. "C:\temp"
- Ensure proper camera connection configuration
- Run the update tool
- Adjust the port to the Camera Link port where your camera is attached
- Press "check" button. Your camera and its current firmware should show up
- Load the new firmware file
- Press "update". Wait until the process has finished. Do not touch the system while doing the upgrade!

4 Connectors

4.1 Camera Link™

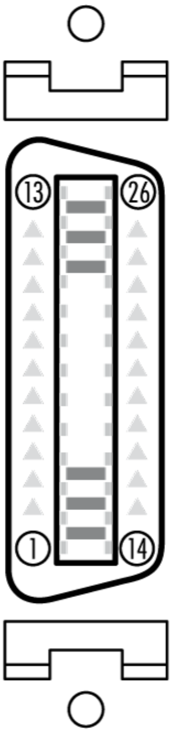


To use Camera Link a frame grabber is needed. Matching frame grabbers can be purchased at your distributor or at SVS-VISTEK.

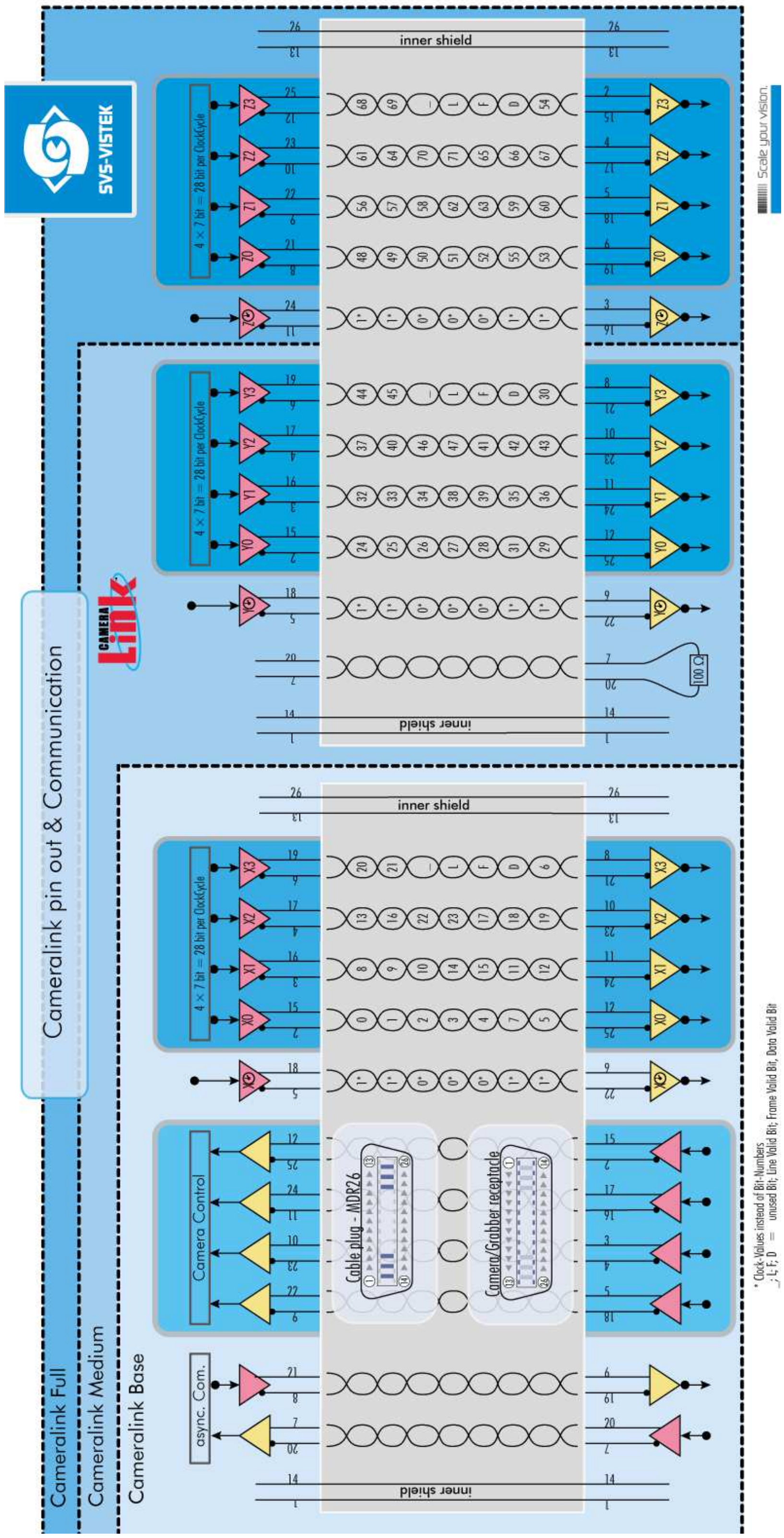
4.1.1 Camera Link Connector

Specification	
Type	26 Pin connector SDR female
Manufacturer	3M
Part-Nr. connector	12226-1150-00FR
Operating Mode	Camera Link™ with RS 232 communication

4.1.2 CameraLink Pinout



Pinout Pin	Signal Name	Direction	Signal Description
- 1 -	GND / 12 V	-	Shield 1 / 12 V power*
- 2 -	X0-	Camera to FG	Data
- 3 -	X1-	Camera to FG	Data
- 4 -	X2-	Camera to FG	Data
- 5 -	Xclk-	Camera to FG	Transmitter Clock / PVAL
- 6 -	X3-	Camera to FG	Data
- 7 -	SerTC+	FG to Camera	Camera Control (RS232)
- 8 -	SerTFG-	Camera to FG	Camera Control (RS232)
- 9 -	CC1-	FG to Camera	ExSync
- 10 -	CC2+	FG to Camera	Prin (not used)
- 11 -	CC3-	FG to Camera	External Camera Clock
- 12 -	CC4+	FG to Camera	nc
- 13 -	GND	-	Shield 3 / power return*
- 14 -	GND	-	Shield 2 / power return*
- 15 -	X0+	Camera to FG	Data
- 16 -	X1+	Camera to FG	Data
- 17 -	X2+	Camera to FG	Data
- 18 -	Xclk+	Camera to FG	Transmitter Clock
- 19 -	X3+	Camera to FG	Data
- 20 -	SerTC-	FG to Camera	Camera Control (RS232)
- 21 -	SerTFG+	Camera to FG	Camera Control (RS232)
- 22 -	CC1+	FG to Camera	Exsync
- 23 -	CC2-	FG to Camera	Prin (not used)
- 24 -	CC3+	FG to Camera	External Camera Clock
- 25 -	CC4 -	FG to Camera	nc
- 26 -	GND / 12 V	-	Shield 4 / 12 V power *

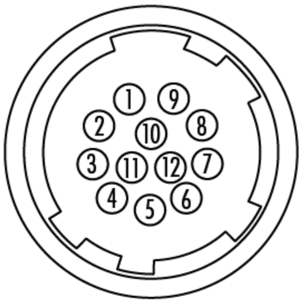

Cameralink pin out & Communication
Cameralink Full
Cameralink Medium
Cameralink Base
SVS-VISTEK
Scale your vision.
inner shield
4 × 7 bit = 28 bit per ClockCycle
Camera Control
async. Com.
Cable plug - MDR26
Camera/Grabber receptacle
100 Ω
* Clock-Values instead of Bit-Numbers
_; L; F; D = unused Bit; Line Valid Bit; Frame Valid Bit, Data Valid Bit

4.2 Input / output connectors

Hirose™ 12Pin

The Hirose connector provides the connectors to power, inputs and outputs. For detailed information about switching lights from inside the camera, refer to strobe control.

Hirose 12 Pin



1	VIN –	(GND)
2	VIN +	(10V to 25V DC)
3	IN4	(RXD RS232)
4	OUT4	(TXD RS232)
5	IN1	(0-24V)
6	IN2	(0-24V)
7	OUT1	(open drain)
8	OUT2	(open drain)
9	IN3 +	(opto In +)
10	IN3 –	(opto In –)
11	OUT3	(open drain)
12	OUT0	(open drain)

Specification	
Type	HR10A-10R-12P
Mating Connector	HR10A-10P-12S

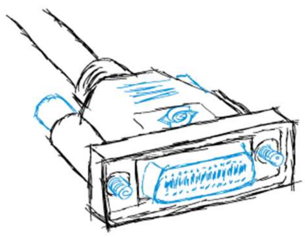
5 The HR Camera Series

Focusing on details

The SVCam HR series is a series of industrial machine vision cameras featuring especially on high image resolutions and high speed at the same without compromising on image quality.

High end CCD and CMOS image sensors with high resolutions permit swift and effortless capture of a wide field of view, making this camera series your prime choice for demanding applications such as optical metrology, quality monitoring, wide field surveillance or traffic monitoring.

Camera Link Features



Camera Link is the most direct interface connection to the camera sensor and preferred by integrators with high demands on bandwidth and especially low latency times.

Please note, as operating Camera Link always involves a frame grabber, the specs given in the specifications might differ from your setup. Please contact us for a recommendation of frame grabbers.

Depending on your camera model, following Camera Link specifics might apply (Please refer to the technical data of your specific camera model):

- There are different transfer rates with different camera link types. Camera Link full (80 bit technology) is also known as “Camera Link Deca”
- Some models support Power over Camera Link (PoCL). Please note, in case you use the 4IO PWM outputs to drive your lights, you need an external power supply as the PoCL is unable to deliver the high currents requested.

4IO adds Light and Functionality

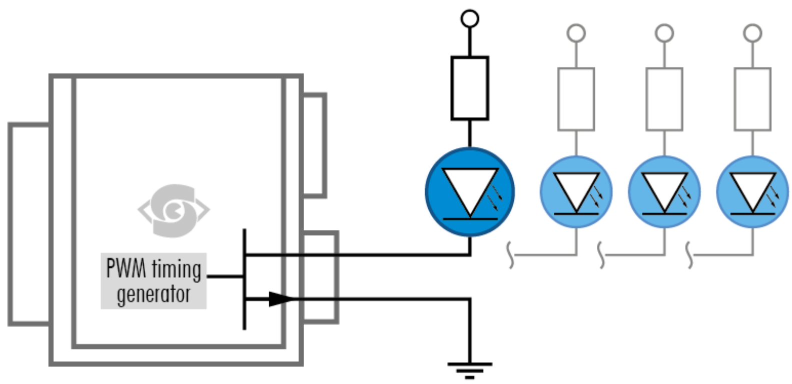


Figure 2: 4IO concept with up to 4 switching LED lights

Your SVS-Vistek camera is equipped with the innovative 4IO-interface allowing full light control, replacing external strobe controllers. Each of the outputs can be individually configured and managed using pulse-width modulation. With its high current output, the camera is able to drive LED lights directly without external light controller.

The integrated sequencer allows multiple exposures with settings to be programmed, creating new and cost effective options. Logical functions like AND / OR are supported.

- Up to 4 x open drain high power OUT

- Up to 4 x high voltage IN – TTL up to 25 Volts
- Power MOSFET transistors
- PWM strobe control
- Sequencer for various configurations
- PLC fuctionality with AND, OR and timers
- Safe Trigger (debouncer, prescaler, high low trigger)

Find an example how to operate light control in the sequencer section.

6 Feature description

This chapter covers features of SVCam cameras. Not every feature might be supported by your specific camera model. For information about the features of your specific model, please refer to the specifications area with your exact model.

6.1 Basic Understanding

6.1.1 Global shutter

The shutter is describing the functionality of exposing the light sensitive pixels of the sensor to light for a limited time. With Global shutterall pixels are exposed to light at the same time. All pixel will be exposed to light at the same starting point, and all pixel light exposure will stop at the same time. Fast moving objects will be captured without showing movement distortion, except motion blur if the moving object is so fast that the same point of the object covers different pixels at start and end of the exposure time in the image.

A global shutter image is a snapshot of the whole scene. Below are illustrations of some images taken with different shutter types. The camera does not move, the bottles are sitting on an assemly line driving by.

Figure 3: moving object,iglobal shutter

Figure 4: moving object, rolling shutter

Figure 5: moving object, interlaced camera

Using flash with global shutter is straight forward: just make sure your flash is on while shutter is open, thus all pixels are exposed to light the same time. You might flash at any time within exposure time.

6.1.2 Exposure speed

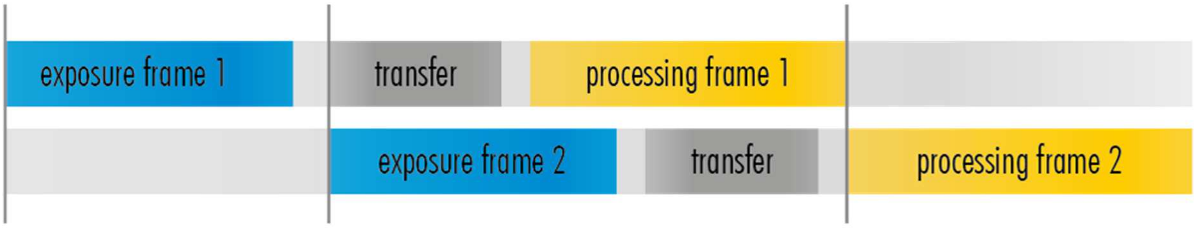
Frames per second, or frame rate describes the number of frames output per second (1/ frame time). Especially GigE and USB cameras cannot guarantee maximum predictable framerates with heavy interface bus load.

Maximum frame rate might depend on

- Pixel clock
- Image size
- Tap structure
- Data transport limitation
- Processing time

6.1.3 Acquisition and Processing Time

The camera has to read the sensor, process the data to a valid image and transfer this to the host computer. Some of these tasks might are done in parallel. This implies the data transfer does not end immediately after end of exposure.



6.1.4 Exposure

See various exposure and timing modes in chapter: Basic capture modes.

Combine various exposure timings with PWM LED illumination, refer to sequencer.

Setting Exposure time

Exposure time can be set by width of the external or internal triggers or programmed by a given value.

6.1.5 Auto exposure

Auto Luminance or auto exposure automatically calculates and adjusts exposure time and gain, frame-by-frame.

The auto exposure or automatic luminance control of the camera signal is a combination of an automatic adjustment of the camera exposure time (electronic shutter) and the gain.

The first priority is to adjust the exposure time and if the exposure time range is not sufficient, gain adjustment is applied. It is possibility to pre-define the range (min. / max. -values) of exposure time and of gain.

The condition to use this function is to set a targeted averaged brightness of the camera image. The algorithm computes a gain and exposure for each image to reach this target brightness in the next image (control loop). Enabling this functionality uses always both – gain and exposure time.

Limitation

As this feature is based on a control loop, the result is only useful in an averaged, continuous stream of images. Strong variations in brightness from one image to next image will result in a swing of the control loop. Therefore it is not recommended to use the auto-luminance function in such cases.

6.1.6 Bit-Depth

Values of brightness are internally represented by numbers. The number of bits for brightness representation is limiting the number of colour values that can be represented. Bit depth defines how many unique colors or grey levels are available in an image after digitization. The number of bits used to quantify limits the number of levels to be used.

$$\text{No of grey values} = 2^{\text{bit depth}}$$

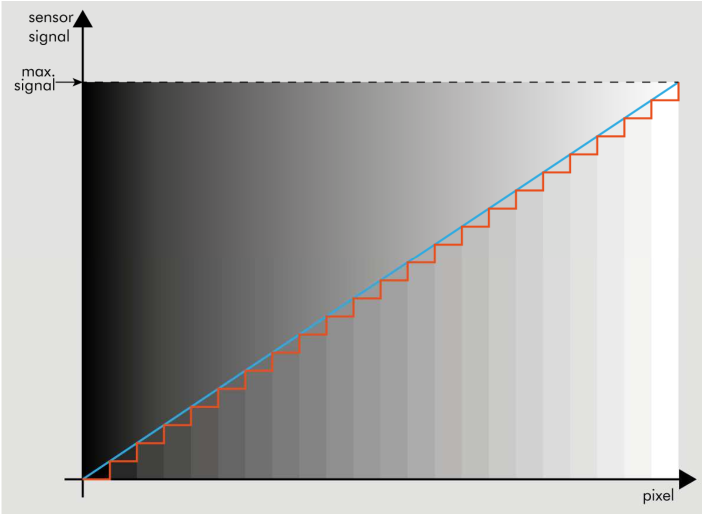


Figure 1: High vs low bit depth representation of brightness values

As SVCams export pure RAW-format only, color has to be created on the host computer in accordance with the known Bayer-pattern by computing the brightness values of RGB into colour values.

6.1.7 Color

Color cameras are identical to the monochrome versions. The color pixels are transferred in sequence from the camera, in the same manner as the monochrome, but considered as "raw"-format.

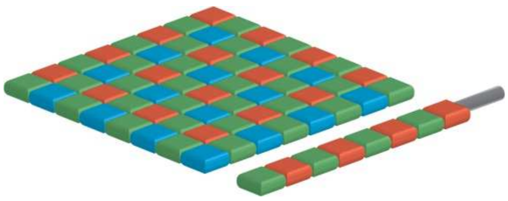


Figure 6: CCD with Bayer Pattern

The camera sensor has a color mosaic filter called "Bayer" filter pattern named after the person who invented it. The pattern alternates as follows:

E.g.: First line: GRGRGR... and so on. (R=red, B=blue, G=green)
Second line: BGBGBG... and so on. Please note that about half of the pixels are green, a quarter red and a quarter blue. This is due to the maximum sensitivity of the human eye at about 550 nm (green).

Using color information from the neighboring pixels the RG and B values of each pixel is interpolated by software. E.g. the red pixel does not have information of green and blue components. The performance of the image depends on the software used.

> **NOTICE**
> It is recommended to use a IR cut filter for color applications!

White Balance

The human eye adapts to the definition of white depending on the lighting conditions. The human brain will define a surface as white, e.g. a sheet of paper, even when it is illuminated with a bluish light.

White balance of a camera does the same. It defines white or removes influences of a color based on a non-white illumination.

6.1.8 Resolution

As mentioned in the specifications, there is a difference between the numerical sensor resolution and the camera resolution. Some pixels towards the borders of the sensor will be used only internally to calibrate sensor values ("dark pixels"). The amount of dark current in these areas is used to adjust the offset.

For calculating image sizes, the maximum camera resolution is determining maximum image resolution. See specifications of your model.

6.1.9 Gain

Setting gain above 0 dB (default) is a way to boost the signal coming from the sensor. Especially useful for low light conditions. Setting gain amplifies the signal of individual or binned pixels before the ADC. Referring to photography adding gain corresponds to increasing ISO. Increasing gain will increase noise as well.

add 6 dB	double ISO value
6 dB	400 ISO
12 dB	800 ISO
18 dB	1600 ISO
24 dB	3200 ISO

Figure 7: Table of dB and corresponding ISO value

NOTICE

Gain also amplifies the sensor's noise. Therefore, gain should be last choice for increasing image brightness. Modifying gain will not change the camera's dynamic range.

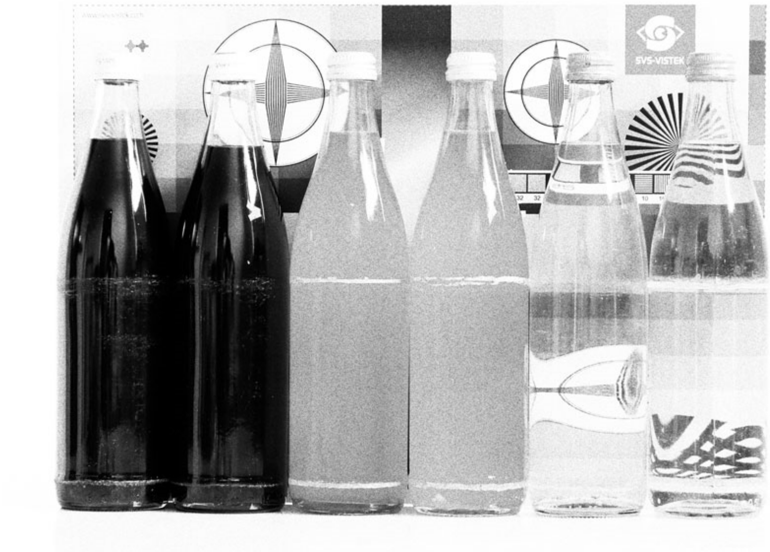


Figure 8: noise caused by too much gain

Auto Gain

For automatic adjustment of Gain please refer to Auto Luminance.

When using autogain with steps of gain the non-continous gain adjustment might be visible in final image. Depending on your application it might be preferrable to use fixed gain values instead and modify exposure with exposure time.

6.1.10 Flip Image

Images can be mirrored horizontally or vertically. Image flip is done inside the memory of the camera, therefore not increasing the CPU load of the PC.

Figure 2: original image

Figure 3: horizontal flip

Figure 4: vertical flip

6.1.11 Decimation

For reducing width or height of an image, decimation can be used. Columns or rows can be ignored.

Refer to AOI for reducing data rate by reducing the region you are interested in.

Figure 9: Horizontal decimation

Figure 10: Vertical decimation

Decimation on Color Sensors

The Bayer pattern color information is preserved with 1/3 horizontal and vertical resolution. The frame readout speed increases approx. by factor 2.5.

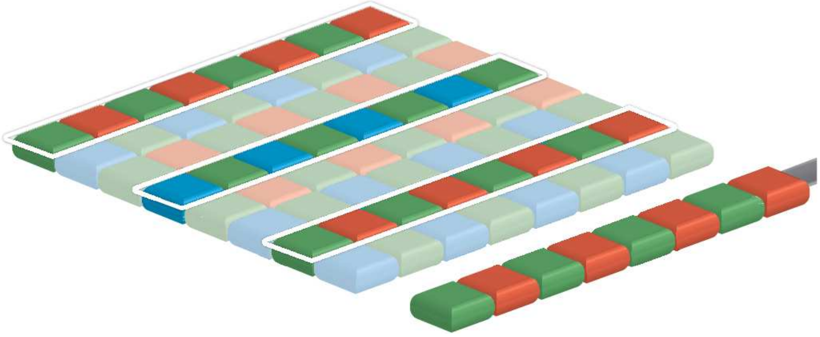


Figure 11: Decimation on color sensors

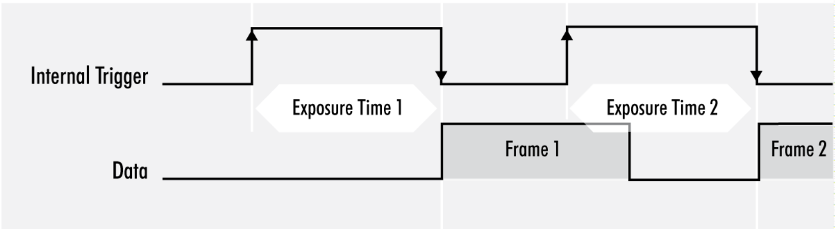
6.2 Camera Features

6.2.1 Basic Capture Modes

Free Running

Free running (fixed frequency) with programmable exposure time. Frames are readout continously and valid data is indicated by LVAL for each line and FVAL for the entire frame.

Mode 0: Free Running with Programmable Exposure Time



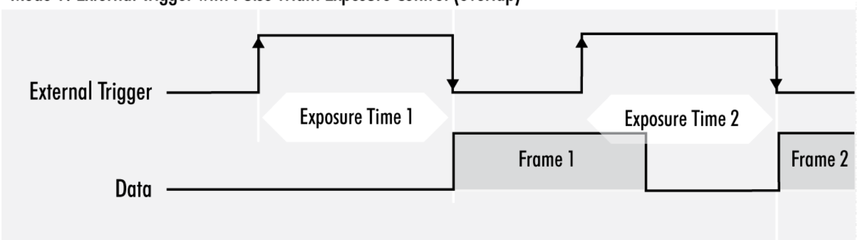
There is no need to trigger the camera in order to get data. Exposure time is programmable via serial interface and calculated by the internal logic of the camera.

> **NOTICE**
> The fundamental signals are:
> Line Valid: LVAL, Frame Valid: FVAL,
> And in case of triggered modes: trigger input.

Triggered Mode (pulse width)

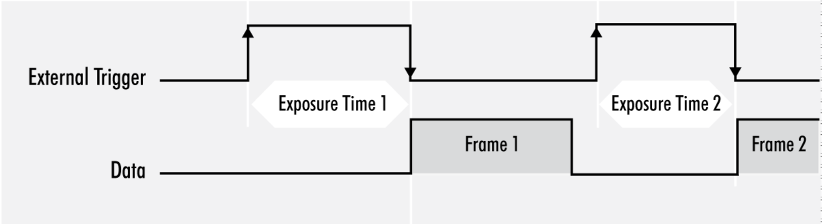
External trigger and pulse-width controlled exposure time. In this mode the camera is waiting for an external trigger, which starts integration and readout. Exposure time can be varied using the length of the trigger pulse (rising edge starts integration time, falling edge terminates the integration time and starts frame read out). This mode is useful in applications where the light level of the scene changes during operation. Change of exposure time is possible from one frame to the next.

Mode 1: External Trigger with Pulse Width Exposure Control (overlap)



Exposure time of the next image can overlap with the frame readout of the current image (rising edge of trigger pulse occurs when FVAL is high). When this happens: the start of exposure time is synchronized to the falling edge of the LVAL signal.

Mode 1: External Trigger with Pulse Width Exposure Control (non overlap)



When the rising edge of trigger signal occurs after frame readout has ended (FVAL is low) the start of exposure time is not synchronized to LVAL and exposure time starts after a short and persistant delay.

The falling edge of the trigger signal must always occur after readout of the previous frame has ended (FVAL is low).

Software Trigger

Trigger can also be initiated by software (serial interface).

NOTICE

Software trigger can be influenced by jitter. Avoid Software trigger at time sensitive applications

External Trigger (Exposure Time)

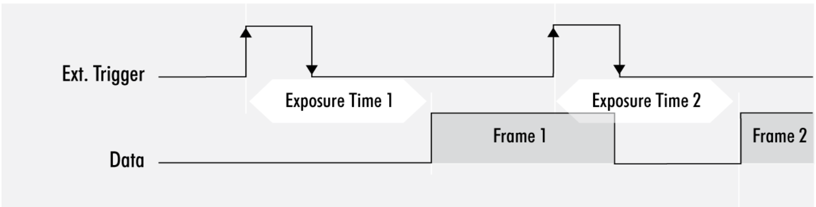
External trigger with programmable exposure time. In this mode the camera is waiting for an external trigger pulse that starts integration, whereas exposure time is programmable via the serial interface and calculated by the internal microcontroller of the camera.

At the rising edge of the trigger the camera will initiate the exposure.

The software provided by SVS-Vistek allows the user to set exposure time e.g. from 60 µs 60 Sec (camera type dependent).

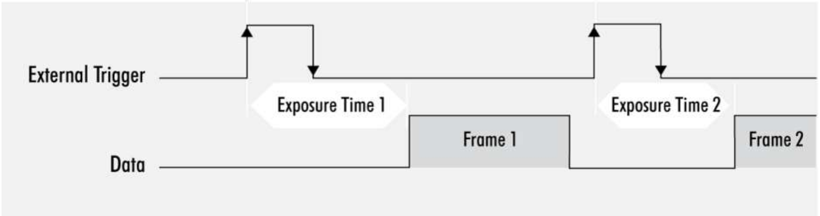
Exposure time of the next image can overlap with the frame readout of the current image (trigger pulse occurs when FVAL is high). When this happens, the start of exposure time is synchronized to the negative edge of the LVAL signal (see figure)

Mode 2: External Trigger with Programmable Exposure Time (overlap)



When the rising edge of trigger signal occurs after frame readout has ended (FVAL is low), the start of exposure time is not synchronized to LVAL and exposure time starts after a short and persistant delay.

Mode 2: External Trigger with Programmable Exposure Time (non overlap)

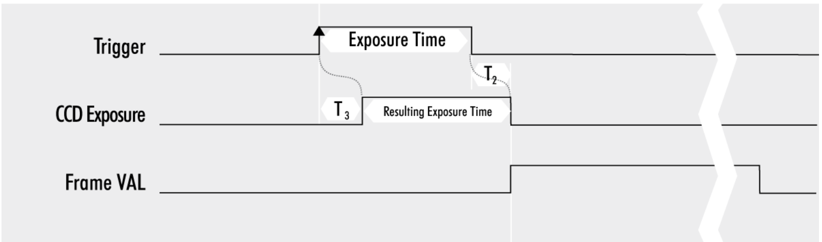


Exposure time can be changed during operation. No frame is distorted during switching time. If the configuration is saved to the EEPROM, the set exposure time will remain also when power is removed.

Detailed Info of External Trigger Mode

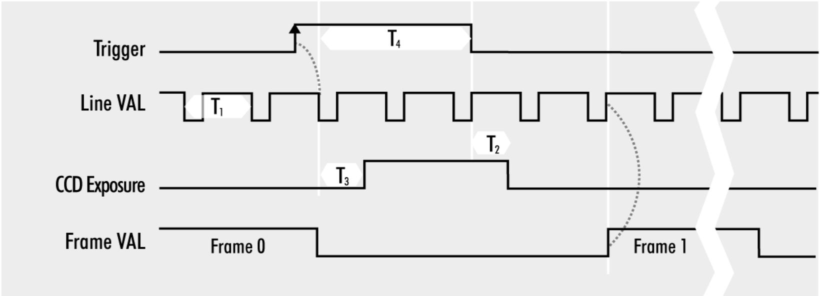
Dagrams below are aquivalent for CCD and CMOS technique.

Mode 1: External Trigger with Pulse Width Exposure Control (non overlap)



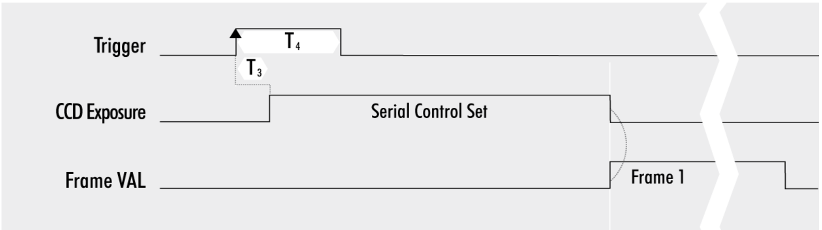
T_1: Line Duration T_2: Transfer Delay T_3: Exposure Delay T_4: min. Trigger Pulse Width

Mode 1: External Trigger with Pulse Width Exposure Control (overlap)



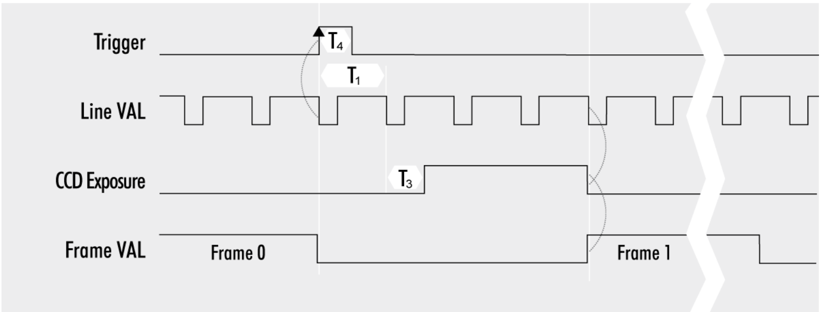
T_1: Line Duration T_2: Transfer Delay T_3: Exposure Delay T_4: min. Trigger Pulse Width

Mode 2: External Trigger with Programmable Exposure Time (non overlap)



T_1: Line Duration T_2: Transfer Delay T_3: Exposure Delay T_4: min. Trigger Pulse Width

Mode 2: External Trigger with Programmable Exposure Time (overlap)



T_1: Line Duration T_2: Transfer Delay T_3: Exposure Delay T_4: min. Trigger

6.2.2 System Clock Frequency

Default system clock frequency in almost every SVCam is set to 66.6 MHz. To validate your system frequency refer to: specifications.

Using the system clock as reference of time, time settings can only be made in steps. In this example, the transfer rate is 66.7 MHz, thus resulting in steps of 15 ns.

$$t = \frac{1}{66.\overline{6}\,MHz} = \frac{1}{66\,666\,666.\overline{6}\,\frac{1}{s}} = 15 \cdot 10^{-9}\,s = 15\,ns$$

NOTICE

Use multiples of 15 ns to write durations into camera memory

6.2.3 Temperature Sensor

A temperature sensor is installed on the mainboard of the camera.

To avoid overheating, the temperature is constantly monitored and read. Besides software monitoring, the camera indicates high temperature by a red flashing LED. (See flashing LED codes)

6.2.4 Camera Link timing

It might be interesting to know when "valid data" can be expected exactly.

px_h = pixel horizontal [count]

px_v = pixel vertical [count]

LVAL – t_{Lvd}

Every line has periods with no valid data. The Duration of None Valid Data between two lines (t_{nvd}) is three time the Camera Link clock (clk). Delay before every first line is 2 times clk.

$$t_{Lvd} = \frac{px_h}{CL\_geometry\_X} \times \frac{1}{CL\_clock}$$

CL_clock = 85 MHz

FVAL – t_{Fvd}

Frames are not sent permanently. Between two frames will be a gap – even at highest frame rates. Minimum duration between two valid frame signals is the duration of one line.

$$t_{Fvd} = 2 \times \frac{1}{CL\_clock} + (t_{Lvd} + t_{nvd}) \times \frac{px_v}{CL\_geometry\_Y}$$

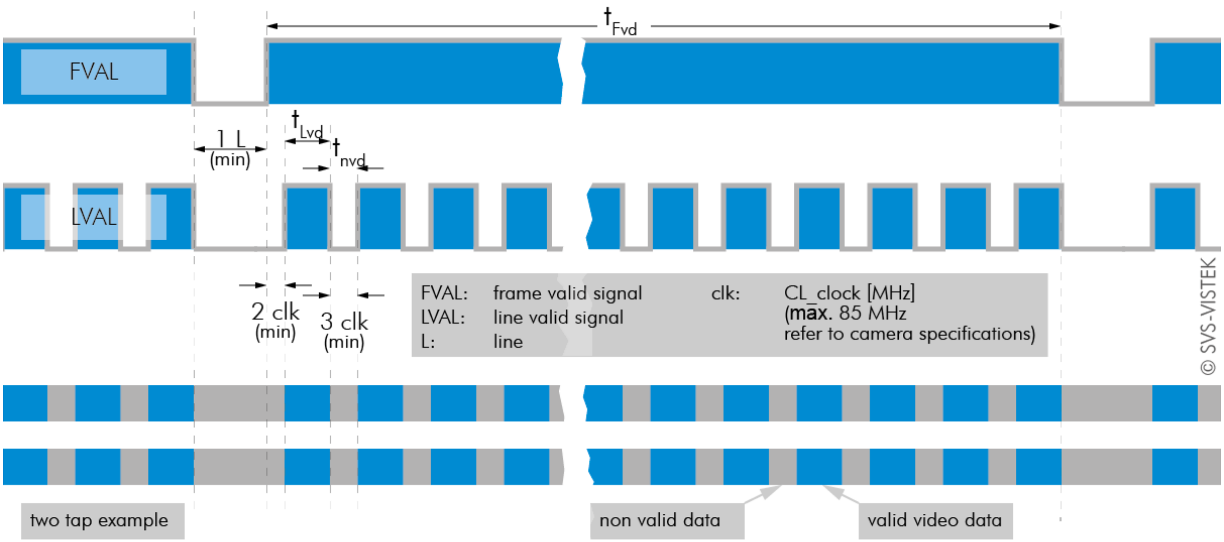


Figure 12: overview of FVAL and LVAL signal timing on Camera Link

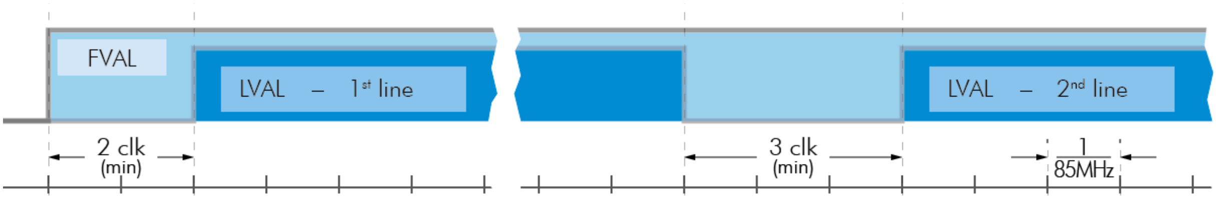


Figure 13: more detailed view of LVAL signal timing on Camera Link

Example calculation									on exo174*CL
>	t_{Lvd}	=	(1920 / 2) px in line / sent at once	×	(1/85MHz) CL_clock				
		=	960	×	(1/85e⁶) s			≈	11,29 µs
>	t_{nvd}	=	3 × (1/85MHz) time between two valid line data packages			=	(3/85e⁶) s	≈	35,3 ns
>	t_{Fvd}	=	2 x (1/85MHz) delay before first line	+	(t_{LVd}	+	t_{nvd})	×	1200 lines [count]
		=	(2/85e⁶) s	+	(11,29 µs	+	35,3 ns)	×	1200
		=	23,5 ns	+	(11,29 µs	+	35,3 ns)	×	1200
		=	(2 + (960 + 3) × 1200) s			/	85e⁶	≈	13,6 ms

Camera Link architecture exo174*CL: 1X2_1Y count = 2
pixelh =1920 pixelv = 1200 CL_clock = 85 MHz

Figure 14: example calculation of Camera Link timing on a exo174*CL

6.2.5 ROI / AOI

In Partial Scan or Area-Of-Interest or Region-Of-Interest (ROI) -mode only a certain region of the sensor will be read.

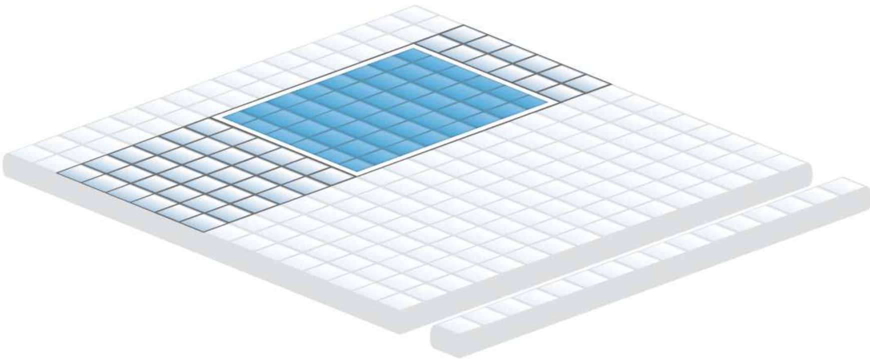


Figure 15: AOI on a CCD sensor

Selecting an AOI will reduce the number of horizontal lines being read. This will reduce the amount of data to be transferred, thus increasing the maximum speed in term of frames per second.

With CCD sensors, setting an AOI on the left or right side does not affect the frame rate, as lines must be read out completely.

With CMOS Sensors, AOI can be selected as well. Please note, most CMOS sensors require the camera to read full horizontal sensor lines internally. Reducing horizontal size with AOI might result in limited fps speed gain.

6.2.6 Defect Pixel Correction

All image sensor have defect pixels in a lesser or greater extent. Type and number of defects determine the quality grade (quality classification) of the sensor.

Defect Pixel Correction is using information from neighboring pixels to compensate for defect pixels or defect pixel clusters (cluster may have up to five defect pixels).

Defect Pixels either be dark pixels, i.e. that don't collect any light, or bright pixels (hot pixel) that always are outputting a bright signal.

The amount of hot pixels is proportional to exposure time and temperature of the sensor.

By default, all known defect pixels or clusters are corrected by SVS-VISTEK as a factory default.

Under challenging conditions or high temperature environments defect pixel behaviour might change. This can be corrected.

- A factory created defect map (SVS map), defying known defects, is stored in the camera.
- A custom defect map can be created by the user. A simple txt file with coordinates has to be created. The user must locate the pixel defects manually.
- The txt file can be uploaded into the camera. Beware of possible Offset!
- Defect maps can be switched off to show all default defects, and switched back on to improve image quality.

Unlike Shading Correction, Defect Pixel Correction suppresses single pixels or clusters and reconstructs the expected value by interpolating neighboring pixel values. The standard interpolation algorithm uses the pixel to the left or to the right of the defect. This simple algorithm prevents high runtime losses.

More sophisticated algorithms can be used by software.

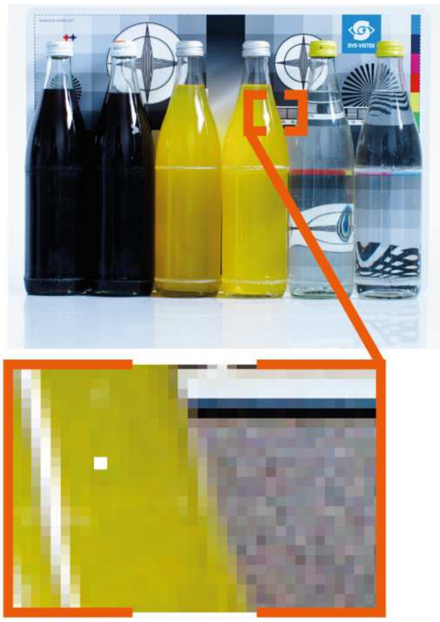


Figure 16: Illustration of a defect pixel

6.2.7 Shading Correction

The interactions between objects, illumination, and the camera lens might lead to a non-uniform flatfield in brightness. Shading describes the non-uniformity of brightness from one edge to the other or center towards edge(s).

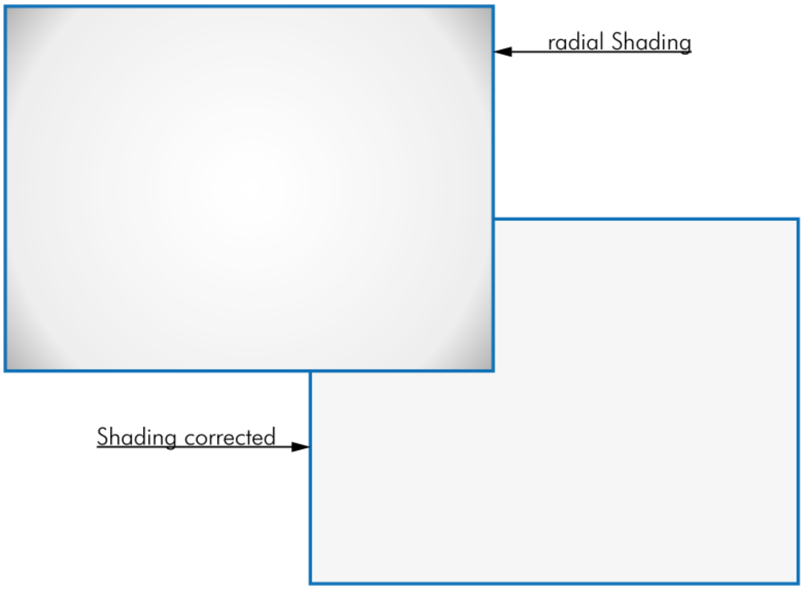


Figure 17: Original and shading corrected image

This shading can be caused by non-uniform illumination, non-uniform camera sensitivity, vignetting of the lens, or even dirt and dust on glass surfaces (lens).

Shading correction is a procedure to create a flatfield image out of a non-uniform image regardless of the reasons of the non-uniformity. Before doing shading correction, make sure your lens is clean and in perfect condition. If the lens is not clean or the lighting not uniform, the algorithm tries to compensate these as well – resulting in a wrong shading table and visible artifacts, loss of details or local noise in the final image.

In theory there are several ways to correct shading:

- In the host computer: Significant loss of dynamic range, colour ruptures
- In the camera, digital: better (smoother) shading than on the computer side (10 or 12 bit), loss of dyn range
- In the camera, analog: Change gain/offset locally on sensor to get optimum shading correction with only small changes in dynamic range

Correct shading with Shading Tool

Images taken with shading correction will seem to have a perfectly balanced illumination. The original idea was to correct the shading of sensor and lens, but it can be used to correct shading of illumination (a non-homogenous illumination) as well.

Shading correction is not a replacement for correct illumination. It is important to have in mind that illumination shading correction might reduce dynamic range of the images taken. By using different gains and offsets on the sensor local noise might be less uniform. Structures in the reference image might lead to visible shading artifacts.

In contrary to any shading correction being done after image recording, the method described here will hardly affect the dynamic range of the image.

The task is done with shading maps. Being applied before final pixel quantization, a significant improvement in image quality is the result.

SVCamImgCorrTool (you might download this from the SVS-Vistek download center) will help you through this task.

6.3 I/O Features

6.3.1 PWM

Pulse width modulation

Description of the function used within the sequencer or implemented by the pulseloop module

During Pulse Width Modulation, a duty cycle is modulated by a fixed frequency square wave. This describes the ratio of ON to OFF as duty factor or duty ratio.

Why PWM?

Many electrical components must be provided with a defined voltage. Whether it's because they do not work otherwise or because they have the best performance at a certain voltage range (such as diodes or LEDs).

Diode characteristic

Since LEDs have a bounded workspace, the PWM ensures a variable intensity of illumination at a constant voltage on the diodes.

In addition, the lifetime of a diode increases. The internal resistance is ideal in this area. The diode gets time to cool down when operated with a PWM in its workspace.

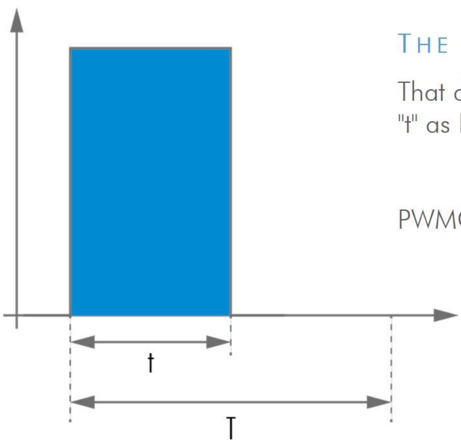
Implementation of PWM

The basic frequency of the modulation is defined by the cycle duration "T".

$$T_{PWM} = \frac{1}{f_{PWM}}$$

Duty cyle "T" is written into the registry by multiple of the inverse of camera frequency. (15 ns steps) Refer to: Time unit of the camera.

$$T_{PWM} = \frac{1}{66,\overline{6}MHz} \cdot \text{PWMMax[SeqSelector]}$$
$$= 15\,ns \cdot \text{PWMMax[SeqSelector]}$$



The intensity of a PWM:

That duty ratio is calculated as: Δ% = t / T. It is written about the value of "t" as PWMChange0-3[SeqSelector] per sequence into the Registry.

PWMChange0-3[SeqSelector] is to be written as a percentage value.

Examples of PWMs:

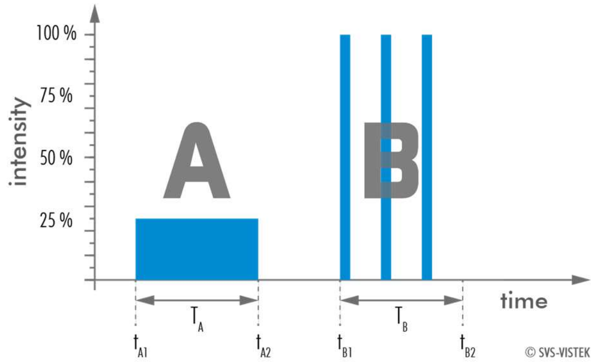


Figure 18: 25% PWM load

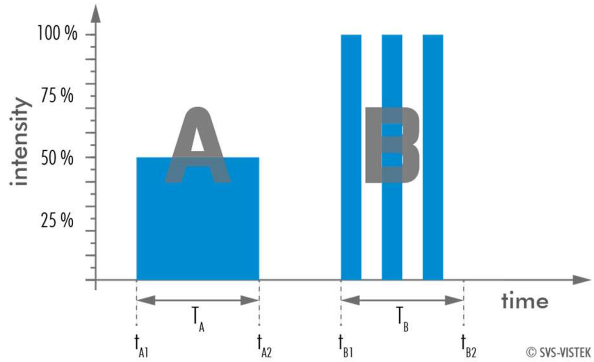


Figure 19: 50% PWM load

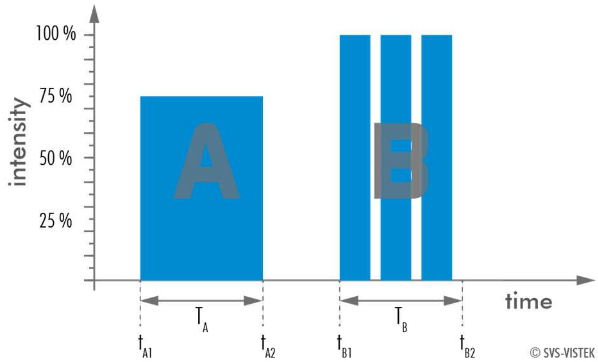


Figure 20: 75% PWM load

The integrals over both periods T_A and T_A are equal.

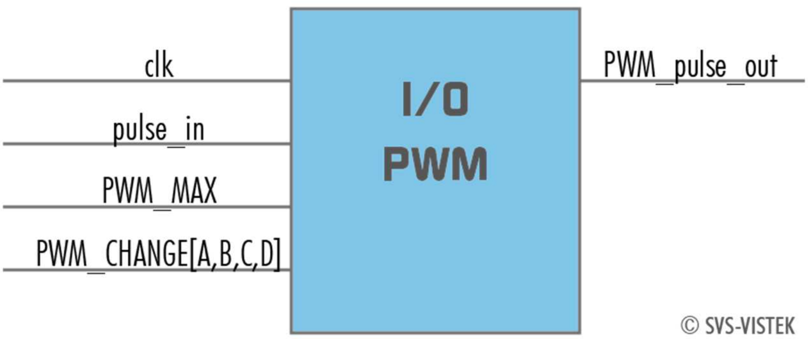
$$\int_{t_{A1}}^{t_{A2}} \mathbf{A} = \int_{t_{B1}}^{t_{B2}} \mathbf{B}$$

An equal amount of Photons will be emitted. The intensity of light is the same.

$$t_{A2} - t_{A1} = t_{B2} - t_{B1}$$

The periods T_A and T_B are equal in length.

The PWM module:



6.3.2 Assigning I/O Lines – IOMUX

The IOMUX is best described as a switch matrix. It connects inputs, and outputs with the various functions of SVCam I/O. It also allows combining inputs with Boolean arguments.

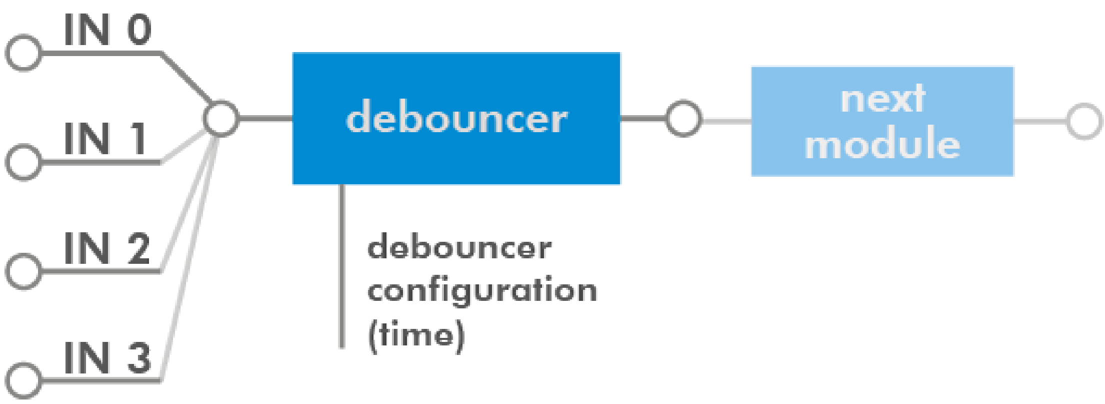


Figure 21: "IN0" connected to "debouncer"

The input and output lines for Strobe and Trigger impulses can be arbitrarily assigned to actual data lines. Individual assignments can be stored persistently to the EPROM. Default setting can be restored from within the Camera.

LineSelector	translation
Line0	Output0
Line1	Output1
Line2	Output2
Line3	Output3
Line3	Output4
Line5	Uart In
Line6	Trigger
Line7	Sequencer
Line8	Debouncer
Line9	Prescaler
Line10	Input0
Line11	Input1
Line12	Input2
Line13	Input3
Line14	Input4
Line15	LogicA
Line16	LogicB
Line17	LensTXD
Line18	Pulse0
Line19	Pulse1
Line20	Pulse2
Line21	Pulse3
Line22	Uart2 In

Note:

If you connect the camera with a non-SVS-Vistek GigEVision client, you might not see the clearnames of the lines, but only line numbers. In this case, use this list of line names

Refer to pinout in input / output connectors when physically wiring.

input vector to switch matrix

nr.	name	description
0	io_in(0)	trigger input 0 – 24 Volt / RS-232 / opto *
1	io_in(1)	trigger input 0 – 24 Volt / RS-232 / opto *
2	io_in(2)	trigger input 0 – 24 Volt / RS-232 / opto *
3	io_in(3)	trigger input 0 – 24 Volt / RS-232 / opto *
4	io_rxd input	
5	txd_from_uart1	input
6	strobe(0)	output from module iomux_pulseloop_0
7	strobe(1)	output from module iomux_pulseloop_1
8	rr_pwm_out_a	output from module iomux_sequenzer_0
9	rr_pwm_out_b	output from module iomux_sequenzer_0
10	expose input	
11	readout input	
12	r_sequenzer_pulse_a	output from module iomux_sequenzer_0 (pulse)
13	rr_pwm_out_c	output from module iomux_sequenzer_0
14	rr_pwm_out_d	output from module iomux_sequenzer_0
15	r_sequenzer_active	output from module iomux_sequenzer_0
16	r_debouncer	output from module iomux_dfilter_0
17	r_prescaler	output from module iomux_prescaler_0
18	r_sequenzer_pulse_b	output from module iomux_sequenzer_0 (pwmmask)
19	r_logic	output from module iomux_logic_0
20	strobe(2)	output from module iomux_pulseloop_2
21	strobe(3)	output from module iomux_pulseloop_3
22	mft_rxd input	
23	trigger_feedback	input
24	txd_from_uart2	input

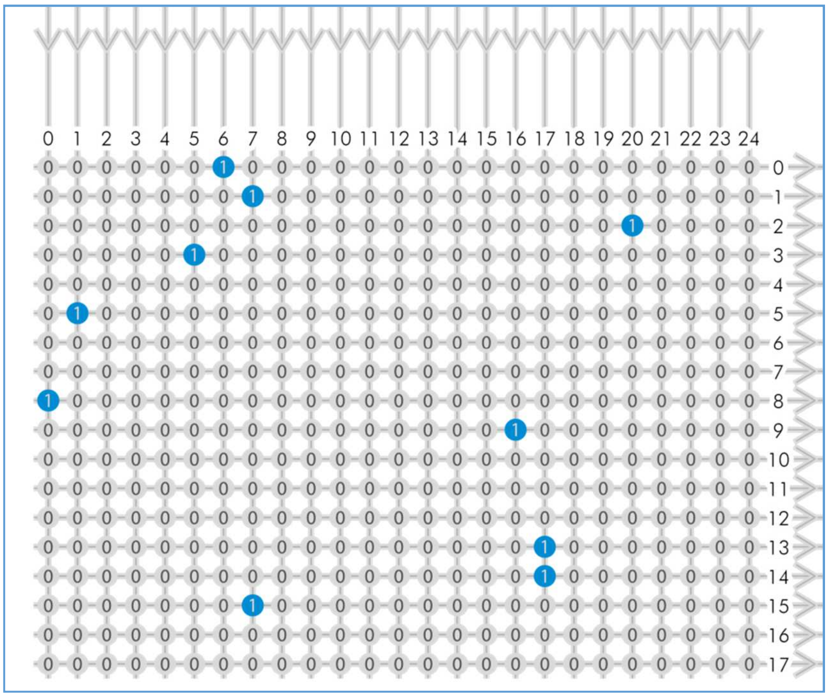
* refer to pinout or specifications

output vector from switch matrix

nr.	name / register	describtion
0	io_out(0)	output open drain
1	io_out(1)	output open drain
2	io_out(2)	output open drain *
3	io_out(3)	output open drain *
4	io_txd	output, when debug='0'
5	rxd_to_uart1	output (uart_in)
6	trigger	output
7	sequenzer_hw_trigger	input to module iomux_sequenzer_0
8	debounce input	input to module iomux_dfilter_0
9	prescale input	input to module iomux_prescaler_0
10	logic inputa	input to module iomux_logic_0
11	logic inputb	input to module iomux_logic_0
12	mft_txd	output
13	pulseloop hw_trigger	input to module iomux_pulseloop_0
14	pulseloop hw_trigger	input to module iomux_pulseloop_1
15	pulseloop hw_trigger	input to module iomux_pulseloop_2
16	pulseloop hw_trigger	input to module iomux_pulseloop_3
17	rxd_to_uart2	output (uart2_in)

* for physical number of open drain outputs refer to pinout or specifications

Example of an IOMUX configuration



>The trigger signal comes in on line 0

>Debounce it.
connect line 0 to 8:
1000000000000000000000000
signal appears again on line 15 – debouncer out

>Use the prescaler to act only on every second pulse.
connect line 16 to 9.
0000000000000000100000000
signal appears again on line 17 – debouncer out

>Configure a strobe illumination with pulseloop module 0
connect line 17 to 13
signal from pulse loop module 0 appears on line 6
connect line 6 to 0 (output 0)

>Set an exposure signal with pulseloop module 1.
connect line 17 to 6

>Tell another component that the camera is exposing the sensor.
connect line 17 to 14
signal from pulse loop module 1 appears on line 7
connect line 7 to 1 (output 1)

> Turn of a light that was ON during the time between two pictures.
connect line 17 to 15
invert signal from pulse loop module 2
it appears on line 20
connect line 20 to 2 (output 2)

Inverter & Set-to-1

Inverter and "set to 1" is part of every input and every output of the modules included in the IOMUX.

Inverter

The inverter enabled at a certain line provides the reverse signal to or from a module.

Set to "1"

With set to "1" enabled in a certain line, this line will provide a high signal no matter what signal was connected to the line before.

Set to "1" – invers

The inverse of a set to "1" line will occour as a low signal, regardle the actual signal that came to the inverter modul.

6.3.3 Strobe Control

The SVCam 4I/O concept contains an integrated strobe controller. Its controls are integrated into the GenICam tree. With LED lights attached to the outputs, this enables the user to control the light without external devices. Being controlled via GenICam, any GenICam-compliant 3rd party software is able to control the light as well. Depending on the camera model, up to 4 (see specifications) independent channels are supported with a peak current of max 3 Amps.

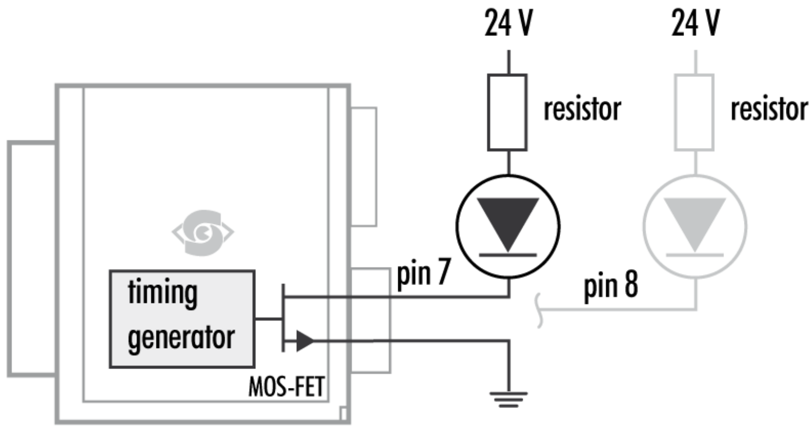


Figure 22: Attach LED lights to camera outputs. For detailed connector pin out refer to Connectors

USE RIGHT DIMENSION OF RESISTOR!

To avoid destruction of your LED light, make sure to use the right dimension of shunt resistor. If not done so, LEDs and/or Camera might be damaged.

Calculate LED shunt resistors

Shunt resistors are used to limit the LED current. Make sure, neither shunt nor LED are run above specs.

LEDs in Continuous Mode

Example Calculation "No Flash" (CW Mode)	
Voltage drop al 5 LEDs, 2,2 V per LED (see spec. of LED)	11 V
Max. continuous current (see spec. of LED)	250 mA
Voltage Supply	24 V
Voltage drop at Resistor (24 V – 11 V)	13 V
Pull up Resistor R = $\frac{13\,V}{250\,mA}$	52 Ω

Total Power ($P = U \times I$)	6 W
Power at LEDs ($11\,V \times 250\,mA$)	2,75 W
Power Loss at Resistor ($13\,V \times 250\,mA$)	3,25 W

LEDs in Flash Mode

Most LED lights can cope with currents higher than specs. This gives you higher light output when light is ON. Please refer to your LED specs if LED overdrive is permitted.

By controlling the duty cycle the intensity of light and current can be controlled. See sequencer example how to adjust the values in the GenICam tree for strobe control.

Current		"time ON" within a 1 Sec		PWM %	
0,75	A	500	ms	50	%
1	A	300	ms	33,3	%
2	A	70	ms	7	%
3	A	40	ms	4	%

Example: If pulse is 1.5 A the max. "on" time is 150 mSec. This translates to "off" time is 850 mSec. The sum of "time on" and "time off" is 1000 mSec = 1 Sec.

NOTICE

The shorter the „time on" – the higher current can be used when driving LEDs with current higher than spec

Strobe vocabulary

For an example how to enable and adjust the integrated strobe controller refer to sequencer. Times and frequencies are set in tics. 1 tic = 15ns.

Exposure Delay

A tic value, representing the time between the (logical) positive edge of trigger pulse and start of integration time.

Strobe Polarity

Positive or negative polarity of the hardware strobe output can be selected.

Strobe Duration

The exposure time of LED lights can be set in tics. The min duration is 1 µsec. The longest time is 1 second.

Strobe Delay

The delay between the (logical) positive edge of trigger pulse and strobe pulse output.

6.3.4 Sequencer

The sequencer is used when different exposure settings and illuminations are needed in a row.

Values to set	Description
Sequencer interval	Duration of the interval
Exposure start	Exposure delay after interval start
Exposure stop	Exposure stop related to interval Start
Strobe start	Strobe delay after interval start
Strobe stop	Strobe stop related to interval Start
PWM frequency	Basic duty cycle (1 / Hz) for PWM
PWM change	Demodulation results

In the current GenICam implementation, all values have to be entered in tic values.

1 tic = 15 ns

Every adjustment (times, frequencies) has to be recalculated into tics and done in tics. See the example below.

When setting “Exposure Start” and “Stop” consider ‘read-out-time’ of the sensor. It has to be within the Sequencer interval.

For physical input and output connections refer to pinout or specifications or see example below. After trigger signal all programmed intervals will start. Up to 16 intervals can be programmed.

Sequencer settings can be saved to camera EEPROM.

Example

For demonstration, imagine following task to be done:

Scenario

An object should be inspected with a monochrome camera. For accentuating different aspects of the image, 4 images should be taken in a row with 4 different colours of light: Red, Green, Blue, White. White light should be generated from the RGB lights being activated at the same time. Basis is a dark environment without other light sources.

Camera wiring

- 3 LED lights are physically connected to the camera on out 0-2 (red, green, blue)
- Out 3 is not used

I/O matrix

- 4 images to be taken (RGBW) result in 4 sequences
- RGB PWM change with different intensities (duty cycle) taking care for differences in spectral response of the camera sensor
- PWM change 0-2 is connected to out 0-2
- Seq pulse A is driving the exposure (trigger)
- Seq pulse B is driving the strobe
- Seq pulse B in WHITE sequence is reduced down to 33% as light intensities of 3 lights (RGB) will add up

Notes

- Different exposure / strobe timings are used for illustration. In most cases they will show values same as exposure
- The resulting exposure time shows the period of sensor light exposure. (''masking'' of exposure time by creating strobe light impulses shorter than exposure time). This value is not adjustable at the camera
- PWM change is shown with reduced height for demonstrating reduced intensity. In reality though, PWM change will be full height (full voltage, shunt resistor might be necessary) with the adjusted duty cycle
- Use a PWM frequency high enough not to interfere with your timings (here: 1000 Hz)

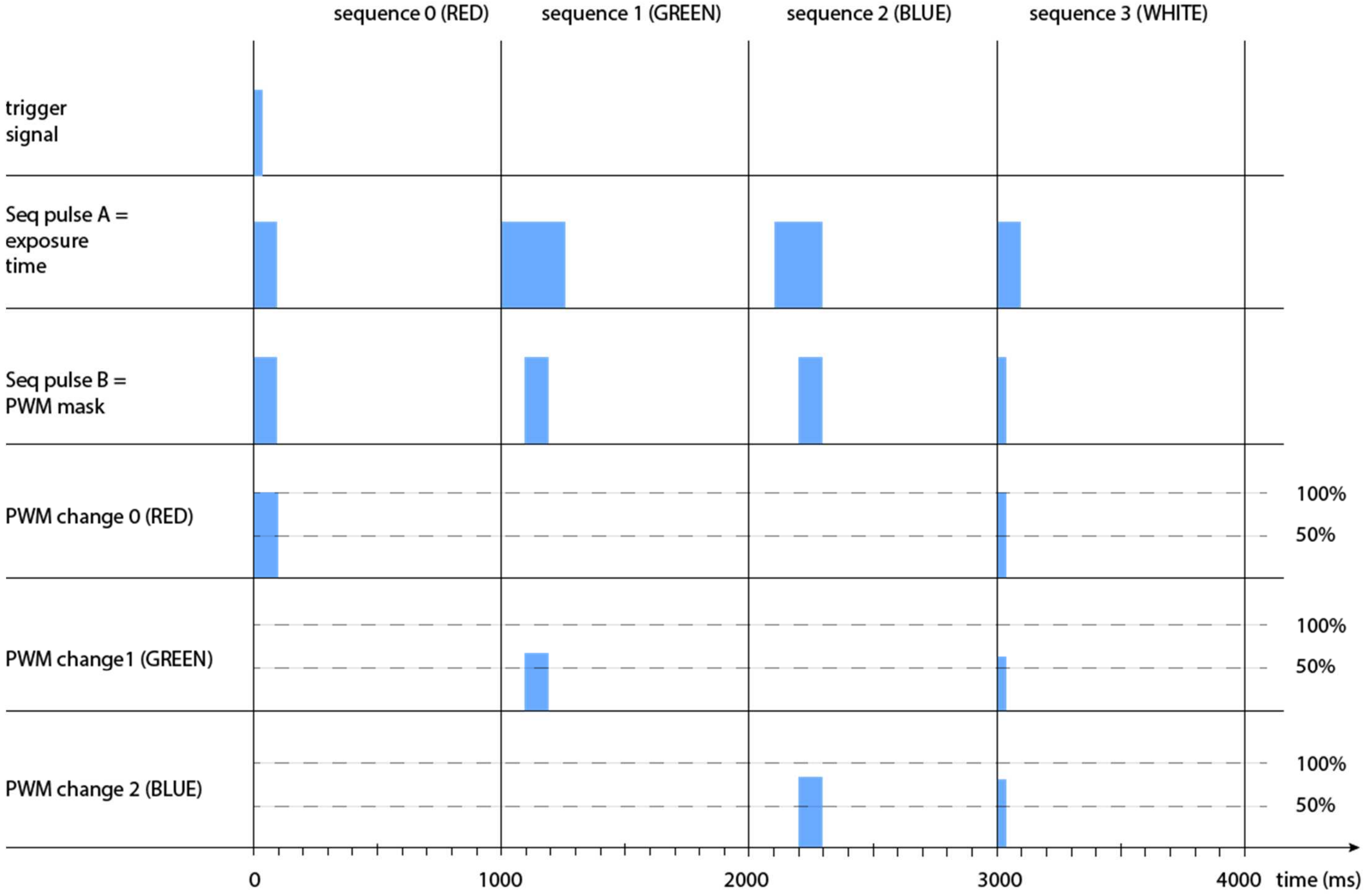
Scenario values	Interval 0 (RED)	Interval 1 (GREEN)	Interval 2 (BLUE)	Interval 3 (WHITE)
Sequencer Interval	1000 ms	1000 ms	1000 ms	1000 ms
Seq pulse A start	0 ms	0 ms	100 ms	0 ms
Seq pulse A stop	100 ms	300 ms	300 ms	100 ms
Seq pulse B start	0 ms	100 ms	200 ms	0 ms
Seq pulse B stop	100 ms	200 ms	300 ms	33 ms
PWM Frequency f	1000 Hz	1000 Hz	1000 Hz	1000 Hz
PWM change 0 (RED)	100%	0%	0%	100%
PWM change 1 (GREEN)	0%	70%	0%	70%
PWM change 2 (BLUE)	0%	0%	80%	80%
PWM change 3	-	-	-	-

As being said before, all these values have to be entered into the camera's GenICam tree as tic values.

The timing values translate like this into tics:

Values to set in GenICam properties	Interval 0 (RED)	Interval 1 (GREEN)	Interval 2 (BLUE)	Interval 3 (WHITE)
Sequencer Interval	66666667 tic (1000 ms)	66666667 tic (1000 ms)	66666667 tic (1000 ms)	66666667 tic (1000 ms)
Seq pulse A start	0 tic (0 ms)	0 tic (0 ms)	6666667 tic (100 ms)	0 tic (0 ms)
Seq pulse A stop	6666667 tic (100 ms)	20000000 tic (300 ms)	20000000 tic (300 ms)	6666667 tic (100 ms)
Seq pulse B start	0 tic (0 ms)	6666667 tic (100 ms)	13333333 tic (200 ms)	0 tic (0 ms)
Seq pulse B stop	6666667 tic (100 ms)	13333333 tic (200 ms)	20000000 tic (300 ms)	2200000 tic (33 ms)
Effective exposure time	100 ms	100 ms	100 ms	33 ms
PWM Frequency f	66667 tic (1000 Hz)	66667 tic (1000 Hz)	66667 tic (1000 Hz)	66667 tic (1000 Hz)
PWM change 0 (RED)	66667 tic (100% of 1000 Hz)	0 tic	0 tic	66667 tic (100% of 1000 Hz)
PWM change 1 (GREEN)	0 tic	46667 tic (70% of 1000 Hz)	0 tic	46667 tic (70% of 1000 Hz)
PWM change 2 (BLUE)	0 tic	0 tic	53333 tic (80% of 1000 Hz)	53333 tic (80% of 1000 Hz)
PWM change 3	-	-	-	-

In a timings diagram, the sequence values above will look like this:



6.3.5 Optical Input

An optical input is designed for galvanic separation of camera and triggering device. Noise, transients and voltage spikes might damage your components. Also trigger signal interpretation can be difficult with unclear voltage potentials within a system. The benefit of an optical input is to avoid all these kinds of interaction from power sources or switches. The disadvantage of an optical input is that it is slower in terms of signal transmission and slew rate than a direct electrical connection.

An optical input needs some current for operation. The SVS-Vistek optical input is specified to 5-24V, 8mA.

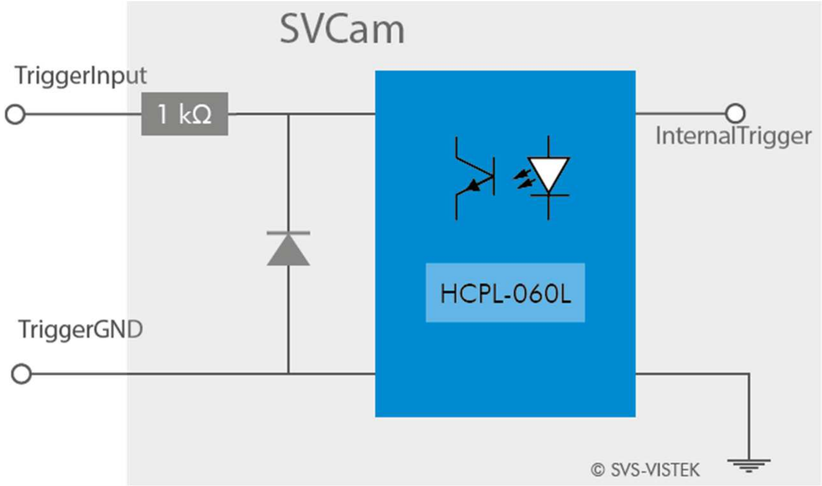
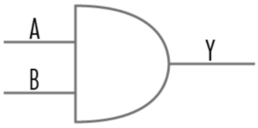


Figure 5 Optical input schematics

The opto coupler galvanically divides electrical circuits by emitting light on one side and interpreting light in the other. There is no direct electric interaction between both electrical circuits.

6.3.6 PLC/Logical Operation on Inputs

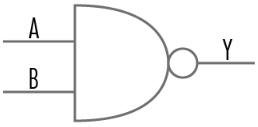
The logic input combines trigger signals with Boolean algorithms. The camera provides AND, NAND, OR, NOR as below. You might connect 2 signals on the logic input. The result can be connected to a camera trigger signal or it may be source for the next logical operation with another input. It is possible to connect it to an OUT line as well.



AND

Both trigger inputs have to be true.

A	B	Y = A ∧ B
0	0	0
0	1	0
1	0	0
1	1	1

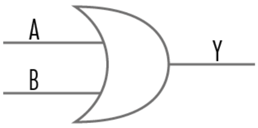


NAND

The NEGATIVE-AND is true only if its inputs are false.

Invert the output of the AND module.

A	B	Y = A NAND B
0	0	1
0	1	1
1	0	1
1	1	0

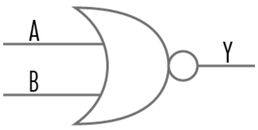


OR

If neither input is high, a low pulse_out (0) results.

Combine trigger input one and two.

A	B	Y = A v B
0	0	0
0	1	1
1	0	1
1	1	1



NOR

No trigger input – one nor two – results in a high or a low level pulse_out.

Invert both trigger inputs. By inverting the resulting pulse_out you will get the NOR I pulse

A	B	Y = A ⊽ B	NOR	Y = A v B	NOR i
0	0	1		0	
0	1	0		1	
1	0	0		1	
1	1	0		1	

6.3.7 Serial data interfaces

(ANSI EIA/) TIA-232-F

RS-232 and RS-422 (from EIA, read as Radio Sector or commonly as Recommended Standard) are technical standards to specify electrical characteristics of digital signaling circuits.

In the SVCam's these signals are used to send low-power data signals to control light or lenses (MFT).

Table 2: serial interface parameter – RS-232 and RS-422

Serial interface Parameter	RS-232	RS-422
Maximum open-circuit voltage	±25 V	±6 V
Max Differential Voltage	25 V	10 V
Min. Signal Range	±3 V	2 V
Max. Signal Range	±15V	10 V

RS-232

It is splitted into 2 lines receiving and transferring Data.

RXD receive data

TXD transmit data

Signal voltage values are:

low: -3 … -15 V

high: +3 … +15 V

With restrictions: refer to Table: serial interface parameter above.

Data transportis asynchronous. Synchronization is implemented by fist and last bit of a package. Therefore the last bit can be longer, e.g. 1.5 or 2 times the bit duration). Datarate (bits per second) must be defined before transmission.

UART

Packaging Data into containers (adding start and stop bits) is implemented by the UART (Universal Asynchronous Receiver Transmitter)

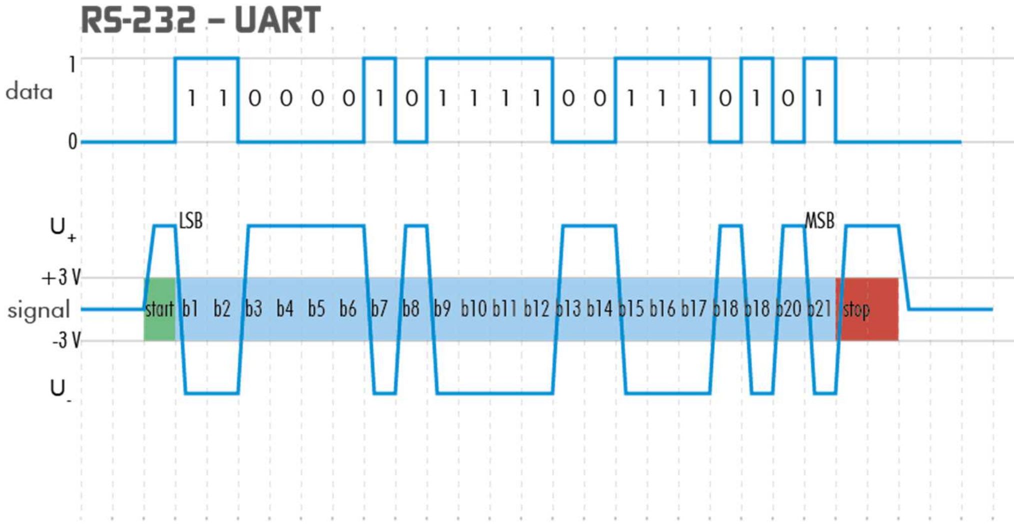


Figure 23: UART encoding of a data stream

RS-422

RS-422 is a differential low voltage communication standard.

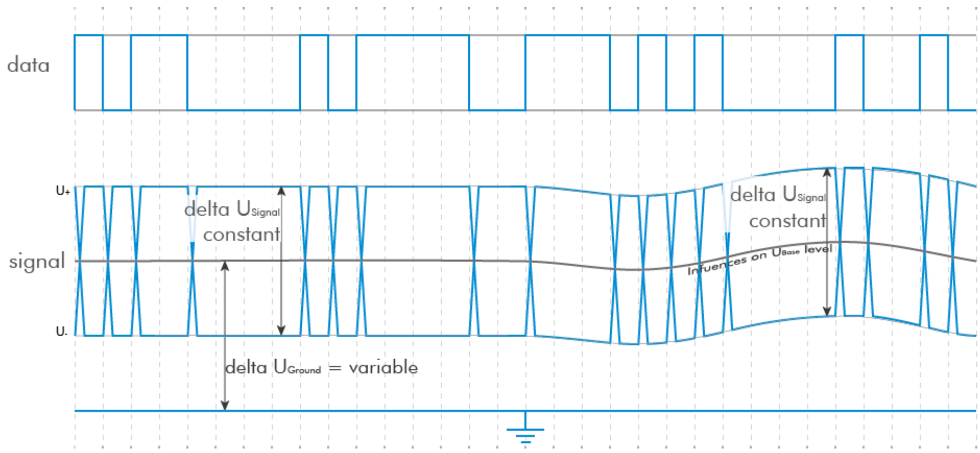


Figure 24: LVDS signal – no return to zero volt

Refer to specifications to see if RS-422 is implemented in your camera.

6.3.8 Trigger-Edge Sensitivity

Trigger-Edge Sensitivity is implemented by a “schmitt trigger”. Instead of triggering to a certain value Schmitt trigger provides a threshold.

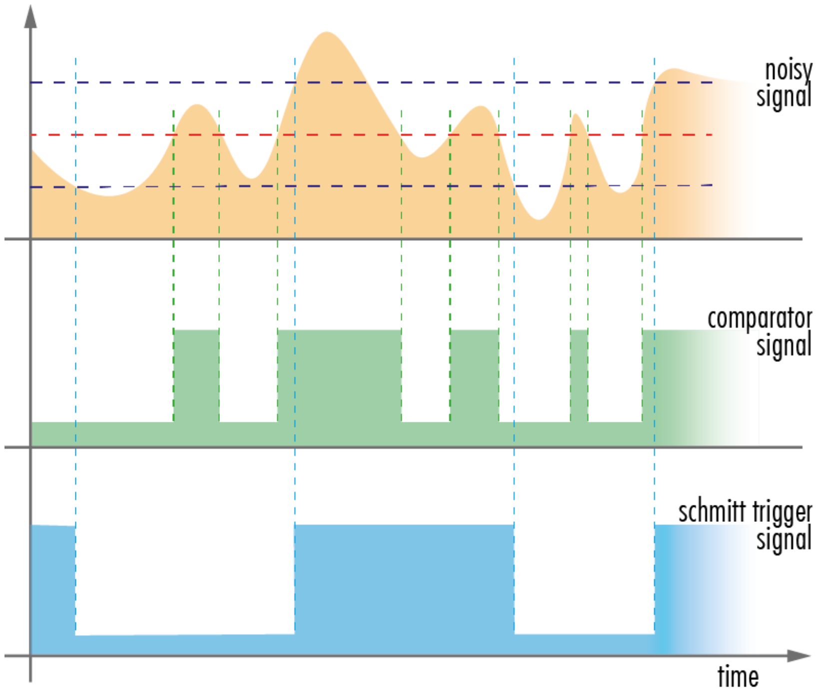


Figure 6: Schmitt trigger noise suppression

6.3.9 Debouncing Trigger Signals

Bounces or glitches caused by a switch can be avoided by software within the SVCam.

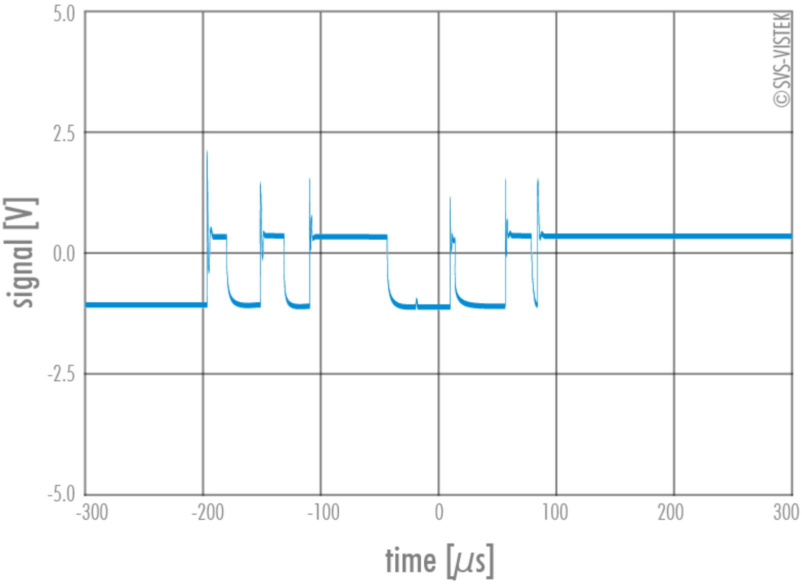


Figure 25: bounces or glitches caused by a switch

Therefor the signal will not be accepted till it lasts at least a certain time.

Use the IO Assignment tool to place and enable the debouncer module in between the "trigger" (schmitt trigger) and the input source (e.g.: line 1).

DebouncDuration register can be set in multiples of 15ns (implement of system clock). E.g. 66 666 ≈ 1 ms

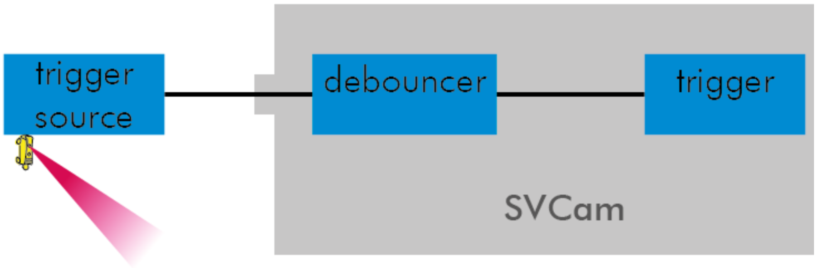


Figure 26: debouncer between the trigger source and trigger

The Debouncer module

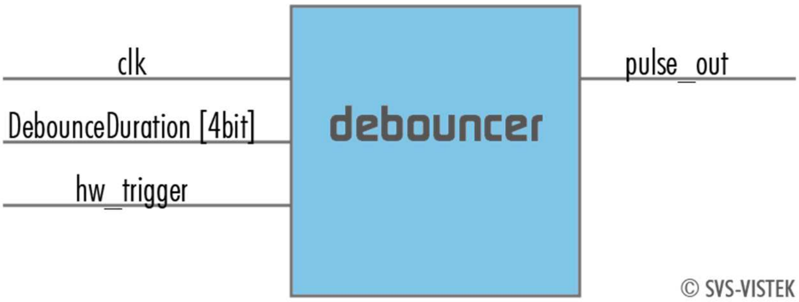


Figure 27: Illustration of the debouncer module

6.3.10 Prescale

The Prescaler function can be used for masking off input pulses by applying a divisor with a 4-bit word, resulting in 16 unique settings.

> Reducing count of interpreted trigger signal

> Use the prescaler to ignore a certain count of trigger signals.

> Divide the amount of trigger signals by setting a divisor.

> Maximum value for prescale divisor: is 16 (4 bit)

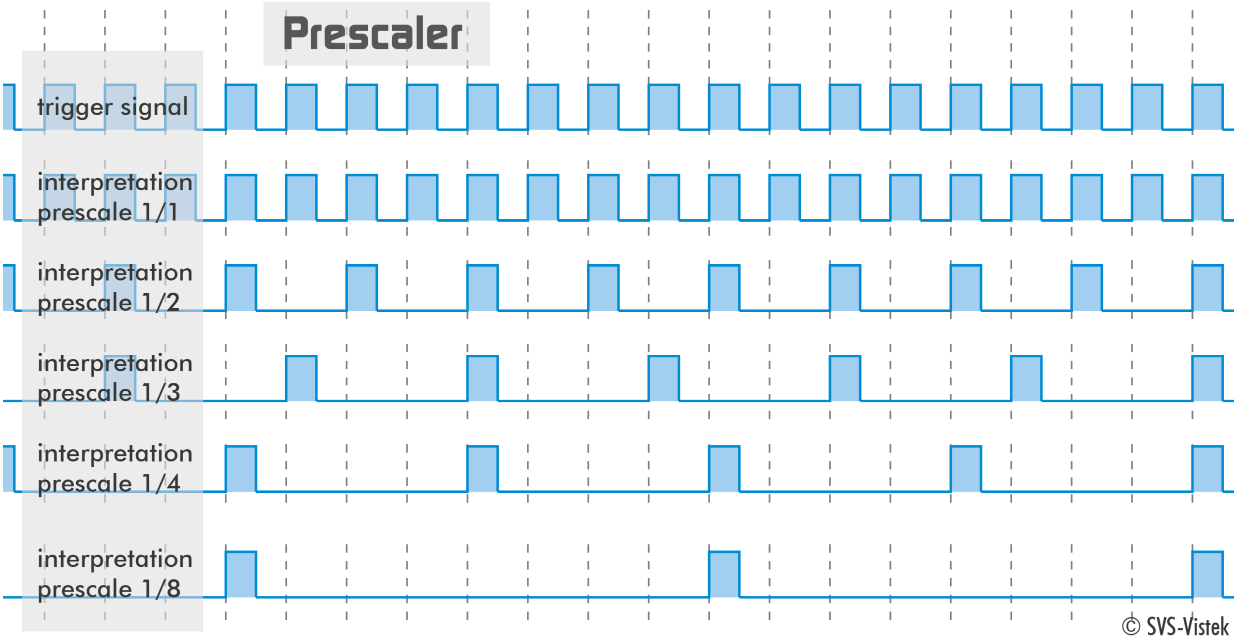


Figure 28: Prescale values

The prescale module

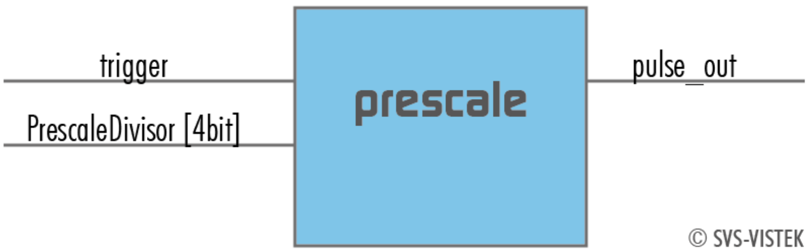


Figure 29: Illustration of the prescale module

7 Specifications

All specifications can be viewed as well on our website, www.svs-vistek.com. We are proud to have ongoing development on our cameras, so specs might change and new features being added.

7.1 hr25*CL

Model	hr25MCL	hr25CCL
family	HR	HR
active pixel w x h	5120 x 5120	5120 x 5120
max. frame rate	31 fps	31 fps
chroma	mono	color
interface	Camera Link 80 Bit	Camera Link 80 Bit

sensor name	NOIP1SN025KA-GDI	NOIP1SE025KA-GDI
sensor manufacturer	ON Semiconductor	ON Semiconductor
sensor architecture	Area CMOS	Area CMOS
shutter type	global	global
equivalent format	35 mm	35 mm
diagonal	32.6 mm	32.6 mm
pixel w x h	4.5x4.5 µm	4.5x4.5 µm
optic sensor w x h	23.04x23.04 mm	23.04x23.04 mm
exposure time	21 µs / 1s	21 µs / 1s
max. gain	18 dB	18 dB
dynamic range		
S/N Ratio		

frame buffer	512MB RAM 160MB Flash	512MB RAM 160MB Flash
CL_geometry	8x-1y;10x-1y	8x-1y;10x-1y
frequency select	-	-
exp. time adjustment	manual;auto;external	manual;auto;external
px format 8 / 12 / 16	x / - / -	x / - / -
packed readout	-	-
max binning h / v	2 / 2	2 / 2
LUT	10to8(1)	10to8(1)
ROI	-	-
white balancing	-	auto;manual
tap balancing	-	-
gain	auto;manual	auto;manual
black level	manual	manual
PIV	-	-
readout control	-	-
flat field correction	x	x
shading correction	external	external
defect pixel correction	true	true
image flip	horizontal;vertical	horizontal;vertical

trigger int / ext / soft	x / x / x	x / x / x
trigger edge high / low	x / x	x / x
sequencer	x	x
PWM power out	x	x
trigger IN TTL-24 V	2	2
outputs open drain	4	4
optical in / out	1 / -	1 / -
RS-232 in / out	1 / 1	1 / 1
RS-422 in / out	- / -	- / -
power supply	10...25 V	10...25 V

lens mount	M58x0.75	M58x0.75
dynamic lens control	-	-
size w / h / d (1)	70x70x51 mm	70x70x51 mm
weight	380 g	380 g
protection class	IP40	IP40
power consumption	10.0 W	10.0 W
operating temperature	-10...65°C	-10...65°C
humidity non-condensing	10…90 %	10…90 %
status	production	production

(1) please refer to drawings

 July 23, 2018 July 23, 2018

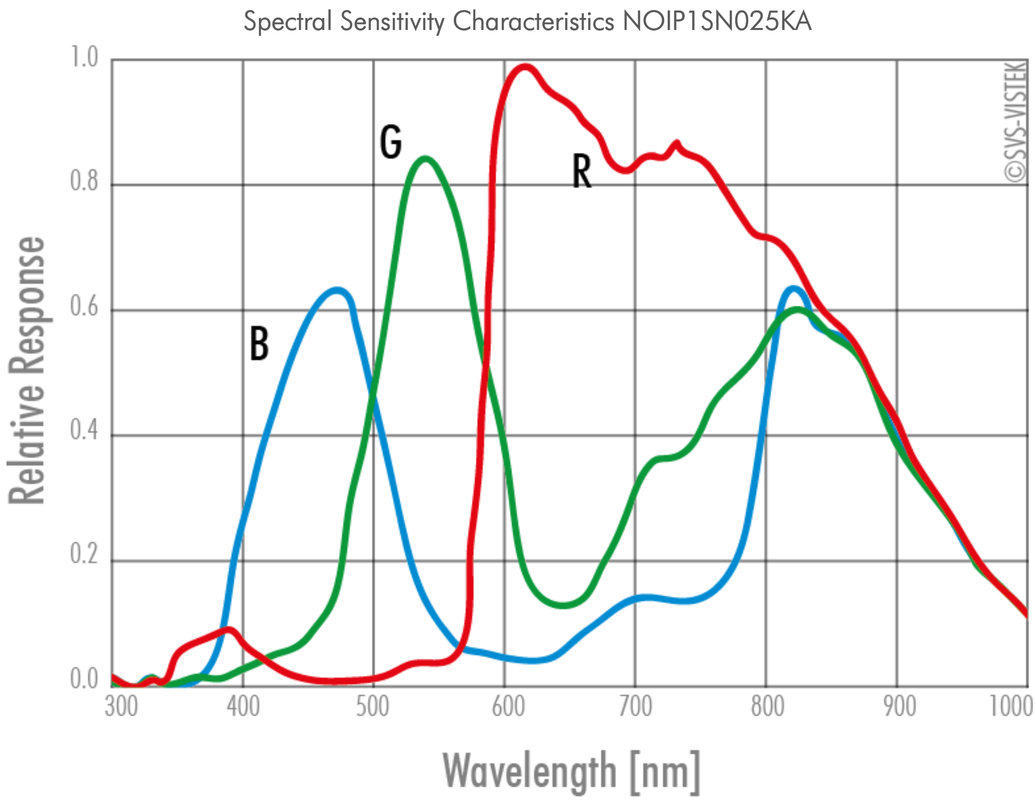


Figure 30: Spectral Sensitivity Characteristics NOIP1SN025KA

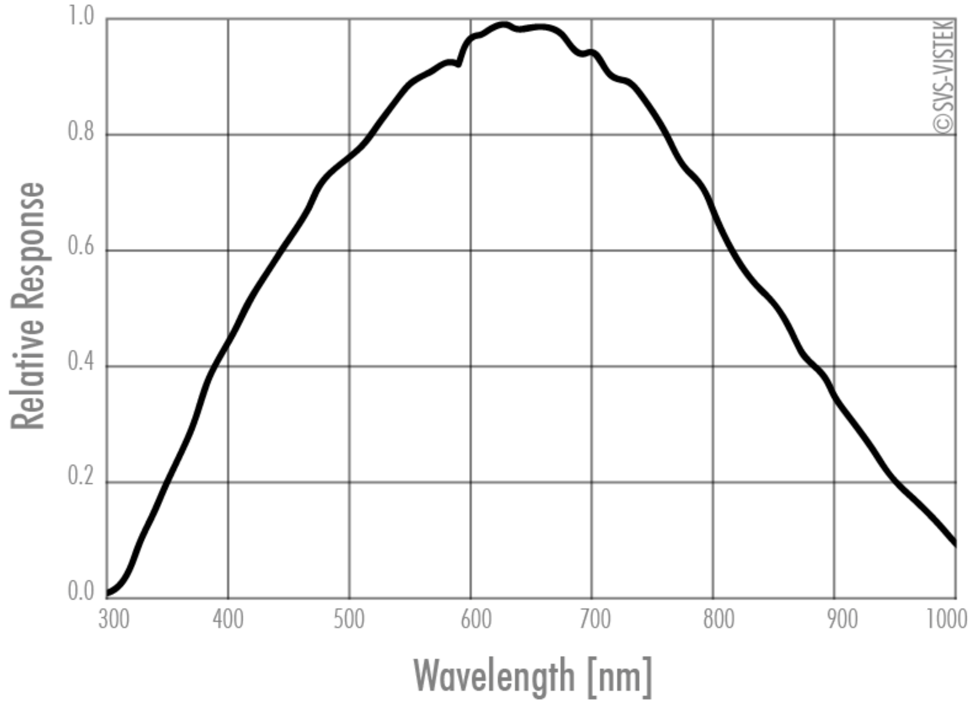


Figure 31: Spectral Sensitivity Characteristics NOIP1SE025KA

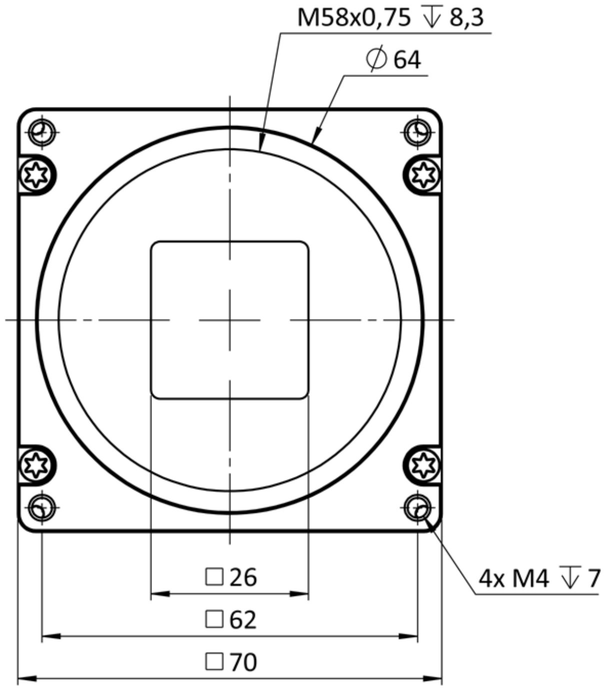
8 Dimensions

All length units in mm.
Find drawings in the web download area at
https://www.svs-vistek.com/en/support/svs-support-download-center.php

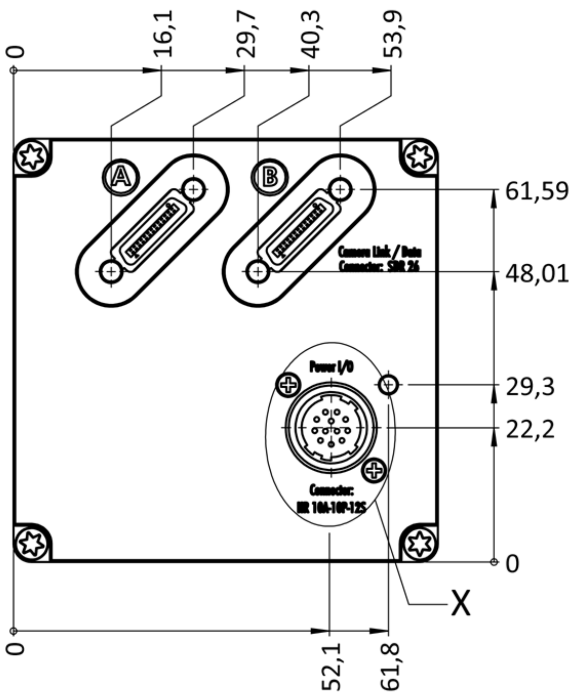
CAD step files available with valid login at SVS-VISTEK.com

8.1 hr25*CL

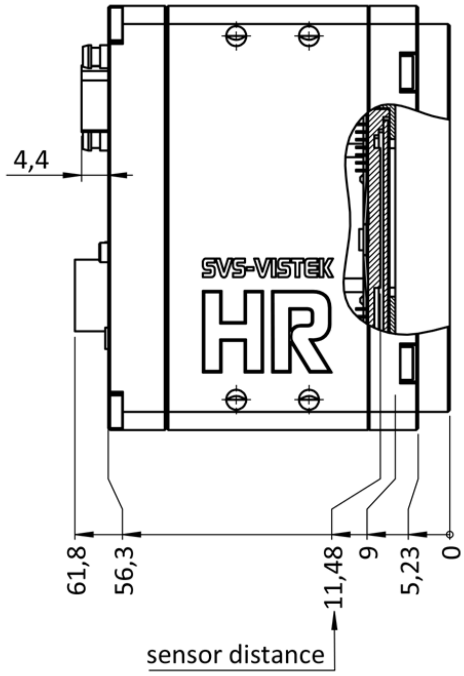
front view



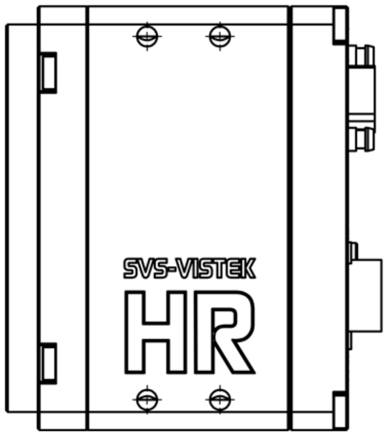
back view



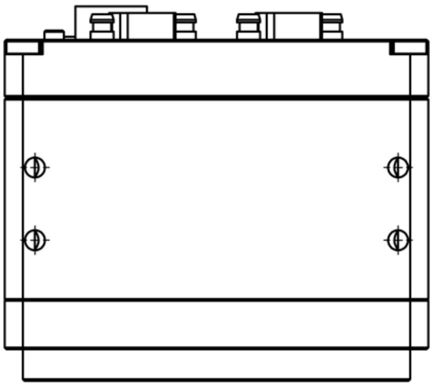
cross section



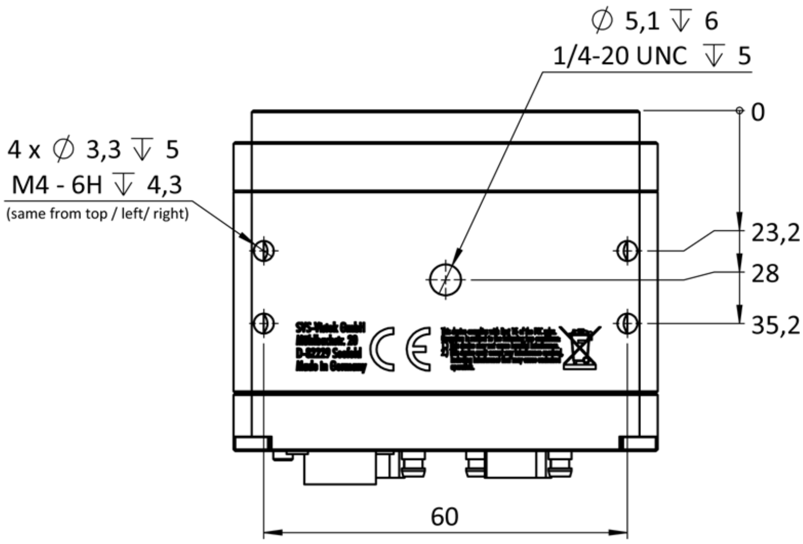
right view



top view



bottom view



8.2 M58 mount

Diameter 58 mm

Thread pitch 0.75 mm

Back focus distance from sensor to flange of the camera: 11.48 mm

Distance from sensor surface to lens differs depending on lens specifications and how far the lens is screwed in.

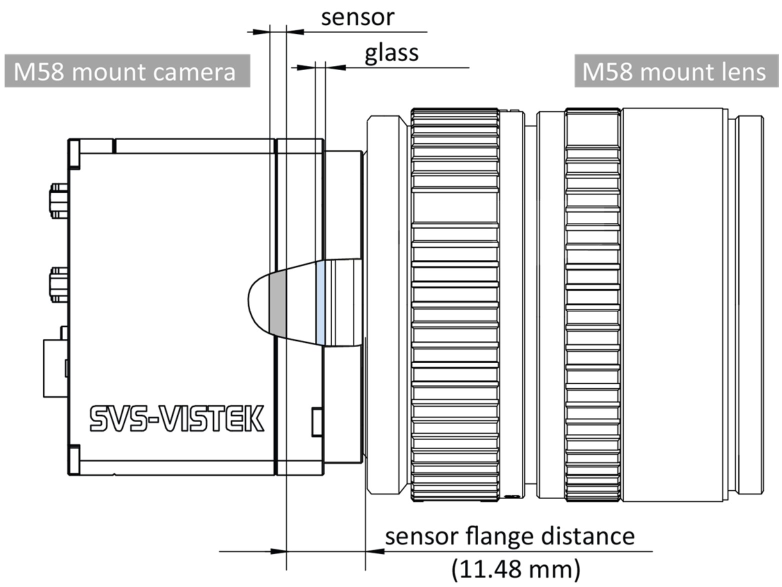


Figure 32: M58-mount

9 Terms of warranty

Standard Products Warranty and Adjustment	Seller warrants that the article to be delivered under this order will be free from defects in material and workmanship under normal use and service for a period of 2 years from date of shipment. The liability of Seller under this warranty is limited solely to replacing or repairing or issuing credit (at the discretion of Seller) for such products that become defective during the warranty period. In order to permit Seller to properly administer this warranty, Buyer shall notify Seller promptly in writing of any claims,; provide Seller with an opportunity to inspect and test the products claimed to be detective. Such inspection may be on customer's premises or Seller may request return of such products at customer's expense. Such expense will subsequently be reimbursed to customer if the product is found to be defective and Buyer shall not return any product without prior return authorization from Seller. If a returned product is found to be out of warranty or found to be within the applicable specification, Buyer will have to pay an evaluation and handling charge, independent of possible repair and/or replacement costs. Seller will notify Buyer of the amount of said evaluation and handling charges at the time the return authorization is issued. Seller will inform Buyer of related repair and/or replacement costs and request authorization before incurring such costs. Buyer shall identify all returned material with Sellers invoice number, under which material has been received. If more than one invoice applies, material has to be clearly segregated and identified by applicable invoice numbers. Adjustment is contingent upon Sellers examination of product, disclosing that apparent defects have not been caused by misuse, abuse, improper installation of application, repair, alteration, accident or negligence in use, storage, transportation or handling. In no event shall Seller be liable to Buyer for loss of profits, loss of use, or damages of any kind based upon a claim for breach of warranty.
Development Product Warranty	Developmental products of Seller are warranted to be free from defects in materials and workmanship and to meet the applicable preliminary specification only at the time of receipt by Buyer and for no longer period of time in all other respects the warranties made above apply to development products. The aforementioned provisions do not extend the original warranty period of any article which has been repaired or replaced by Seller.
Do not break Warranty Label	If warranty label of camera is broken warranty is void. Seller makes no other warranties express or implied, and specifically, seller makes no warranty of merchantability of fitness for particular purpose.
What to do in case of Malfunction	Please contact your local distributor first.

10 FAQ

Problem	Solution
Camera does not respond to light.	Check if camera is set to "Mode 0". I.e. free running with programmed exposure ctrl. When done, check with the program "Convenient Cam" if you can read back any data from the camera, such as "Mode", "type" of CCD, exposure time settings, etc.. If "Mode 0" works properly, check the signals of the camera in the desired operation mode like "Mode 1" or "Mode 2". In these modes, check if the ExSync signal is present. Please note that a TTL signal must be fed to the trigger connector if it is not provided by the frame grabber (LVDS type). The typical signal swing must be around 5 V. Lower levels will not be detected by the camera... If you use a TTL level signal fed to the "TB 5 connector" check the quality and swing. If these signals are not present or don't have the proper quality, the camera cannot read out any frame (Mode 1 and 2). Beware of spikes on the signal.
Image is present but distorted.	Check the camera configuration file of your frame grabber. Check number of "front- and back porch" pixel. Wrong numbers in configuration file can cause sync problems. Check if your frame grabber can work with the data rate of the camera.
Image of a color version camera looks strange or false colors appear.	If the raw image looks OK, check the camera file to see if the pixels need to be shifted by either one pixel or one line. The image depends on the algorithm used. If the algorithm is starting with the wrong pixel such effects appear.
Colors rendition of a color versions not as expected – especially when using halogen light.	Halogen light contains strong portions of IR radiation. Use cut-off filters at around 730 nm like "Schott KG 3" to prevent IR radiation reaching the CCD.
No serial communication is possible between the camera and the PC.	Use "load camera DLL" and try again.

11 Glossary of Terms

Aberration	Spherical aberration occurs when light rays enter near the edge of the lens; Chromatic aberration is caused by different refractive indexes of different wavelengths of the light. (Blue is more refractive than red)
ADC	Analogue-to-Digital Converter, also known as A/D converter
Aperture	In optics, Aperture defines a hole or an opening through which light travels. In optical system the Aperture determines the cone angle of a bundle of rays that come to a focus in the image plane. The Aperture can be limited by an iris, but it is not solely reliant on the iris. The diameter of the lens has a larger influence on the capability of the optical system.
Bayer Pattern	A Bayer filter mosaic or pattern is a color filter array (CFA) deposited onto the surface of a CCD or CMOS sensor for capturing RGB color images. The filter mosaic has a defied sequence of red, green and blue pixels such that the captured image can be transported as a monochrome image to the host (using less bandwidth); where after the RGB information is recombined in a computer algorithm.
Binning	Binning combines the charge from two (or more) pixels to achieve higher dynamics while sacrifying resolution.
Bit-Depth	Bit-depth is the number of digital bits available at the output of the Analog-to-Digital Converter (ADC) indicating the distribution of the darkest to the brightest value of a single pixel.
Camera Link	Camera Link is a multiple-pair serial communication protocol standard [1] designed for computer vision applications based on the National Semiconductor interface Channel-link. It was designed for the purpose of standardizing scientific and industrial video products including cameras, cables and frame grabbers.
CCD	Charge Coupled Device. Commonly used technology used for camera sensors used to detect & quantify light, i.e. for capturing images in an electronic manner. CCDs were first introduced in the early 70ies.
CMOS	Complementary Metal–Oxide–Semiconductor. A more recently adopted technology used for camera sensors with in-pixel amplifiers used to detect & quantify light, i.e. capturing images in an electronic manner.
CPU	Central Processing Unit of a computer. Also referred to as the processor chip.
dB	Decibel (dB) is a logarithmic unit used to express the ratio between two values of a physical quantity.
Decimation	For reducing width or height of an image, decimation can be used (CMOS sensors only). Columns or rows can be ignored. Image readout time is thereby reduced.
Defect map	Identifies the location of defect pixels unique for every sensor. A factory generated defect map is delivered and implemented with each camera.
EPROM	Erasable Programmable Read Only Memory is a type of memory chip that retains its data when its power supply is switched off.
External Trigger	Erasable Programmable Read Only Memory is a type of memory chip that retains its data when its power supply is switched off.
fixed frequency	or programmed exposure time. Frames are read out continuously.
Gain	In electronics, gain is a measure of the ability of a two-port circuit (often an amplifier) to increase the power or amplitude of a signal from the input to the output port by adding energy to the signal.

Gamma	Gamma correction is a nonlinear operation used to code and decode luminance values in video or still image systems.
GenICam	Provides a generic programming interface for all kinds of cameras and devices. Regardless what interface technology is used (GigE Vision, USB3 Vision, CoaXPress, Camera Link, etc.) or which features are implemented, the application programming interface (API) will always be the same.
GigE Vision	GigE Vision is an interface standard introduced in 2006 for high-performance industrial cameras. It provides a framework for transmitting high-speed video and related control data over Gigabit Ethernet networks.
GPU	Graphics Processing Unit of a computer.
Hirose	Cable connectors commonly used for power, triggers, I/Os and strobe lights
ISO	see Gain.
Jumbo Frames	In computer networking, jumbo frames are Ethernet frames with more than 1500 bytes of payload. Conventionally, jumbo frames can carry up to 9000 bytes of payload. Some Gigabit Ethernet switches and Gigabit Ethernet network interface cards do not support jumbo frames.
Mount	Mechanical interface/connection for attaching lenses to the camera.
Multicast	Multicast (one-to-many or many-to-many distribution) is an ethernet group communication where information is addressed to a group of destination computers simultaneously. Multicast should not be confused with physical layer point-to-multipoint communication.
PWM	Pulse width modulation. Keeping voltage at the same level while limiting current flow by switching on an off at a very high frequency.
Partial Scan	A method for reading out fewer lines from the sensor, but "skipping" lines above and below the desired area. Typically applied to CCD sensors. In most CMOS image sensors an AOI (area of interest) or ROI (region of interest) can be defined by selecting the area to be read. This leads to increased frame rate.
Pixel clock	The base clock (beat) that operates the sensor chip is. It is typically also the clock with which pixels are presented at the output node of the image sensor.
RAW	A camera RAW image file contains minimally processed data from the image sensor. It is referred as raw in its meaning. SVS-VISTEK plays out RAW only.
Read-Out-Control	Read-Out control defines a delay between exposure and image readout. It allows the user to program a delay value (time) for the readout from the sensor. It is useful for preventing CPU overload when handling very large images or managing several cameras on a limited Ethernet connection.
Shading	Shading manifests itself a decreasing brightness towards the edges of the image or a brightness variation from one side of the image to the other. Shading can be caused by non-uniform illumination, non-uniform camera sensitivity, vignetting of the lens, or even dirt and dust on glass surfaces (lens).
Shading correction	An in-camera algorithm for real time correction of shading. It typically permits user configuration. By pointing at a known uniform evenly illuminated surface it allows the microprocessor in the camera to create a correction definition, subsequently applied to the image during readout.
Shutter	Shutter is a device or technique that allows light to pass for a determined period of time, exposing photographic film or a light-sensitive electronic sensor to light in order to capture a permanent image of a scene.

Strobe light	A bright light source with a very short light pulse. Ideal for use with industrial cameras, e.g. for "freezing" the image capture of fast moving objects. Can often be a substitute for the electronic shutter of the image sensor. Certain industrial cameras have dedicated in-camera output drivers for precisely controlling one or more strobe lights.
Tap	CCD sensors can occur divided into two, four or more regions to double/quadruple the read out time.
TCP/IP	TCP/IP provides end-to-end connectivity specifying how data should be packetized, addressed, transmitted, routed and received at the destination.
USB3 Vision	The USB3 Vision interface is based on the standard USB 3.0 interface and uses USB 3.0 ports. Components from different manufacturers will easily communicate with each other.
Trigger modes	Cameras for industrial use usually provide a set of different trigger modes with which they can be operated. The most common trigger modes are: (1) Programmable shutter trigger mode. Each image is captured with a pre-defined shutter time; (2) Pulse-Width Control trigger. The image capture is initiated by the leading edge of the trigger pulse and the shutter time is governed by the width of the pulse; (3) Internal trigger or Free-Running mode. The camera captures images at the fastest possible frame rate permitted by the readout time.
XML Files	Extensible Markup Language (XML) is a markup language that defines a set of rules for encoding documents in a format which is both human-readable and machine-readable